INSTRUCTION MANUAL

IM165-U v0.3

Communication Protocol for Data Concentrator-EML16

MODBUS RTU PROTOCOL

Modbus is a master-slave communication protocol able to support up to 247 slaves organized as a bus or as a star network. The physical link layer can be RS232 for a point to point connection or RS485 for a network.

The communication is half-duplex. The network messages can be Query-Response or Broadcast type.

The Query-Response command is transmitted from the Master to an established Slave and generally it is followed by an answering message.

The Broadcast command is transmitted from the Master to all Slaves and is never followed by an answer.

GENERIC MESSAGE STRUCTURE:

START OF FRAME ADDRESS FIELD FUNCTION CODE DATA FIELD ERROR CHECK END OF FRA
START OF FRAME ADDRESS FIELD=Starting message marker =Includes device address in which you ne Response mode. In case the message is a Broadcast type it incluFUNCTION CODE DATA FIELD=Includes the operation code that you need to perform.ERROR CHECK END OF FRAME=Field for the error correction code.
Mode RTU communication frame structure:START OF FRAME= silence on line for time >=4 charactersADDRES FIELD= 1 characterFUNCTION CODE= 1 characterDATA FIELD= N charactersERROR CHECK= 16 bit CRCEND OF FRAME= silence on line for time >=4 characters

Wait time for response:

Request length	1 Register (2 bytes)	32 Register (64 bytes)
Typical	< 100 mSec	< 250 mSec
Worst case	< 150 mSec	< 400 mSec

Reading of the Registers (Function Code \$03)

Reads the binary contents of holding registers (2X references) in the slave. Broadcast is not supported. The Query message specified the starting register and quantity of register to be read.

QUERY:						
	0° Byte	e 1° Byte	2-3° Byte	4-5° Byte	6-7° Byte	
Start of Frame	Address F	ield Function Code	Start Address	Number of Registers	Check Sum	End of Frame
START OF FR.	AME =	Starting message n	narker.			
ADDRESS FIE	LD =	EML16 device addr	ress (0x01 0x	F7)	(1 byte).	
FUNCTION CO	DDE =	Operation code (0x	03)		(1 byte).	
START ADDRE	ESS =	First register addres	ss to be read		(2 byte).	
No. OF REGIS	TERS	= N	umber of regist	ers (max 64 bytes) te	oread	(4 bytes fo
	value).		-			
CHECK SUM	=	Check sum.				

END OF FRAME = End message marker.

<u>Warning</u>: It is possible to read more than one variable at the same time (max 64 bytes) only if their addresses are consecutive and the variables on the same line cannot be divided. The register data in the response message are packet as two bytes per register, with the binary contents right justified within each byte. For each register, the first byte contains the high order bits and the second contains the low order bits.

RESPONSE:

	0° Byte	1° Byte	2° Byte	n° Byte	n+1 - n+2 Byte	
Start of Frame	Address Field	Function Code	Number of Bytes	Data	Check Sum	End of Frame
START OF FRA	ME = Star	rting message m	narker.			
ADDRESS FIEL	D = EML	16 device addr	ess (0x01 0xF7)	(1 byte	ə).
FUNCTION CO.	DE = Ope	eration code (0x	03)		(1 byte	e).
No. OF SEND E	YTES	= N	umber of data by	tes (0x00.	??)	(1 byte).1 register
	requires 2 a	lata bytes.	-	·	,	
D0, D1,, Dn	= data	a bytes (0x00?	??)		(Nr. of	register x 2 = n. byte).
CHECK SUM	= Che	ck sum.				
END OF FRAM	E = End	message mark	er.			

See the TABLE OF Data Concentrator-EML16 REGISTERS and the EXAMPLE.

Setup of the Data Concentrator-EML16 parameters (Function Code \$10)

Write values into a sequence of holding registers (2X references).

<u>Warning</u>: It is possible to write more than one variable at the same time only if their addresses are consecutive and the variables on the same line cannot be divided. (max of 4 consecutive register on the same message).

QUERY:

	0° Byte	1° Byte	2-3° Byte	4-5° Byte	6° Byte	n° Byte	n+1 - n+2 Byte	
Start of Frame	Address Field	Function Code	Start Address	Number of Registers	Number of Bytes	Data	Check Sum	End of Frame
START OF FR/	4 <i>ME</i> = 3	Starting messag	ge marker.					
ADDRESS FIE	LD = E	EML16 device a	address (0x0	1 0xF7)	(1 k	oyte).		
FUNCTION CC	DDE = 0	Operation code	(0x10)	,	(1 k	oyte).		
START ADDRE	ESS = H	First register ad	dress to be w	ritten	(2 byte).			
No. OF REGIS	TER = I	Number of regis	sters to be wr	itten (1 to 4,)	(2 byte).			
No. OF BYTES	;	= Nu	mber of data	bytes (HEX)		(1 by	rte): 1register re	equires 2
da	ata bytes.							
D0,D1,,Dn		Data bytes (0x	00?)		(1 k	oyte) (Nr.	of register x 2 =	= n. byte).
CHECK SUM	-	Check sum.						
END OF FRAM	1E = E	End message n	narker.					

The normal response returns the slave address, function code, starting address and quantity of register preset. RESPONSE:

REDI ONGE.						
	0° Byte	1° Byte	2-3° Byte	4-5° Byte	6-7° Byte	
Start of Frame Ac	Idress Field	Function Code	Start Address	Number of Registers	Check Sum	End of Frame
START OF FRAME	E = Star	ting message m	arker.			
ADDRESS FIELD	= EML	16 device addr	ess (0x01 0)	(F7)	(1 byte).	
FUNCTION CODE		eration code (0x	10)		(1 byte).	
START ADDRESS	= First	t register addres	s to be written		(2 byte).	
No. OF REGISTER	R = Nun	nber of registers	to be written		(2 byte).	
ERROR CHECK	= Che	ck sum.				
END OF FRAME	= End	message mark	er.			

See the TABLE OF Data Concentrator-EML16 REGISTERS and the EXAMPLE.

Broadcast command

It is possible to send a broadcast command (Address Field equal 0x00) for all write command.

QUERY:

	0° Byte	1° Byte	2-3° Byte	4-5° Byte	6° Byte	n° Byte	n+1 - n+2° Byte	
Start of Frame	0x00	Function Code	Start Address	Number of Registers	Number of Bytes	Data	Check Sum	End of Frame

RESPONSE: No Response.

Diagnostic (Function Code \$08)

This function provides a test for checking the communication system. Broadcast is not supported.

The instrument's protocol has only the sub-function 0 of the diagnostics sub-functions set of the standard modbus protocol. The Query and the Response messages are the following:

QUERY:

	0° Byte	1° Byte	2-3° Byte	n° Byte	n+1 - n+2 Byte	
Start of Frame	Address Field	Function Code	Sub Function	Data	Check Sum	End of Frame
START OF FRA	ME = Sta	rting message m	narker.			
ADDRESS FIEL	D = EM	L16 device addr	ess (0x010x	F7)	(1 1	byte).
FUNCTION COL	DE = Op	eration code (0x	(08 ĤEX)		(1)	byte).
SUB FUNCTION	l = Sul	o-function 0 (0x0	0 0x00)		(2 /	byte).
DATA	= Ma	x 10 data bytes.				
CHECK SUM	= Ch	eck sum.				
END OF FRAME	E = Ene	d message mark	er.			

RESPONSE:

The response must be the loopback of the same data.

	0° Byte	Byte 1° Byte 2-3° Byte i		n° Byte	n+1 - n+2° Byte			
Start of Frame A	ddress Field	Function Code	Sub Function	Data	Check Sum	End of Frame		
START OF FRAME = Starting message marker.								
ADDRESS FIELD	= EMI	16 device addr	ess (0x01 0x	:F7)	(1 1	byte).		
FUNCTION CODE			peration code (0x08 HEX)			byte).		
SUB FUNCTION	= Sub	Sub-function 0 (0x00 0x00)			(2	byte).		
DATA	= Data	,	,		,			
CHECK SUM	= Che	ck sum.						
END OF FRAME	= End	message mark	er.					

DIAGNOSTIC EXAMPLE:

QUERY		RESPONSE	
Field Name	Example (Hex)	Field Name	Example (Hex)
Slave Address	0x01	Slave Address	0x01
Function Code	0x08	Function Code	0x08
Sub-function Hi	0x00	Sub-function Hi	0x00
Sub-function Lo	0x00	Sub-function Lo	0x00
Data Hi	0xF1	Data Hi	0xF1
Data Lo	0xA7	Data Lo	0xA7
Error Check (CRC)	0x??	Error Check (CRC)	0x??
	0x??		0x??

Report Slave ID (Function Code \$11)

This function returns the type of the instrument and the current status of the slave run indicator. Broadcast is not supported. The Query and the Response messages are the following:

QUERY:

	0° Byte	<u>}</u>	1° Byte	2 - 3° Byte	
Start of Frame	Address F	ield	Function Code	Check Sum	End of Frame
START OF FRA	ME =	Starti	ing message m	arker.	
ADDRESS FIEL	_D =	EML	16 device addi	ress (0x01	0xF7)
FUNCTION CO	DE =	Opera	ation code (0x	11)	
CHECK SUM	=	Ċhec	k sum.		
END OF FRAM	E =	End r	nessage mark	er.	

RESPONSE:

0° Byte		1° Byte	2° Byte	3° Byte	4° Byte	5° - 6° Byte	
Start of Frame Address Fiel	d	Function Code	Byte Count	Slave ID	Run Indicator Status	Check Sum	End of Frame
START OF FRAME	=	Starting mess	age marker.				
ADDRESS FIELD	=	EML16 device	e address (0)	x01 0xF	7)	(1 byte).	
FUNCTION CODE	=	Operation co	de (0x11)			(1 byte).	
BYTE COUNT	=	Number of da	ta bytes (0x1	16)		(1 byte).	
SLAVE ID	=	Slave ID iden	tifier (0x5F)			(1 byte).	
RUN INDICATOR STATUS	=	Run indicator	status (OxFF	-)		(1 byte).	
DATA	=	Data bytes					
CHECK SUM	=	Check sum.					
END OF FRAME	=	End message	e marker.				

The normal answer has the slave ID identifier (0x5D), the run indicator Status (0xFF), 20 data bytes with the following meaning: 17-18 Byte: Boot Version. -

19-20 Byte: Firmware Version. -

REPORT SLAVE ID EXAMPLE: QUERY

QUERY		RESPONSE	
Field Name	Example (Hex)	Field Name	Example (Hex)
Slave Address	0xXX	Slave Address	0x01
Function Code	0x11	Function Code	0x11
Error Check (CRC)	0x??	Byte count	0x02
	0x??	Slave ID	0x5D
		Run indicator status	0xFF
		Data	20 Bytes
		Error Check (CRC)	0x??
			0x??

Error message from Slave to Master

When a slave device receives a not valid query, it does transmit an error message.

RESPONSE:

RESPUNSE.			
	° Byte 1° Byte 2° Byte	3 - 4° Byte	
	ess Field Function Code Error Code	Check Sum End of Frame	
START OF FRAME	 Starting message marker. 		
ADDRESS FIELD	= EML16 device address (0x01.	0xF7)	(1 byte).
FUNCTION CODE	= Operation code with bit 7 high		(1 byte).
ERROR CODE	= Message containing communication	ation failure	(1 byte).
CHECK SUM	= Check sum.		
END OF FRAME	 End message marker. 		
ERROR EXAMPLE:			
QUERY		RESPONSE	
Field Name	Example (Hex)	Field Name	Example (Hex)
Slave Address	0x01	Slave Address	0x01
Function Code	0x03	Function Code	0x83 (1)
Starting Address Hi	0x00	Error Code	0x02 (2)
Starting Address Lo	0x00	Error Check (CRC)	0x??
Number Of Word Hi	0x00		0x??
Number Of Word Lo	0x05		
Error Check (CRC)	0x??	(1): Function Code	e transmitted by master with bit 7
high.			
	0x??	(2): Error type:	
		0x01 = Illegal Function	
		0x02 = Illegal data ad	
		0x03 = Illegal data val	ue

Data Concentrator-EML16 Registers

The following tables shown all the Data Concentrator-EML16 registers. NOTE: the values are expressed x1000

Register HEX	Register DEC	Word	Description	Value	W/R	Туре
TOTAL COUNTER						
\$100	256	4	TOTAL COUNTER 1	0÷10000000000	R	Unsigned
\$104	260	4	TOTAL COUNTER 2		R	Unsigned
\$108	264	4	TOTAL COUNTER 3		R	Unsigned
\$10C	268	4	TOTAL COUNTER 4		R	Unsigned
\$110	272	4	TOTAL COUNTER 5		R	Unsigned
<u>\$114</u> \$118	276 280	4	TOTAL COUNTER 6 TOTAL COUNTER 7		R R	Unsigned Unsigned
\$11C	284	4	TOTAL COUNTER 8		R	Unsigned
\$120	288	4	TOTAL COUNTER 9		R	Unsigned
\$124	292	4	TOTAL COUNTER 10		R	Unsigned
\$128	296	4	TOTAL COUNTER 11		R	Unsigned
\$12C	300	4	TOTAL COUNTER 12		R	Unsigned
\$130	304	4	TOTAL COUNTER 13		R	Unsigned
<u>\$134</u> \$138	308 312	4	TOTAL COUNTER 14 TOTAL COUNTER 15		R R	Unsigned Unsigned
\$130 \$13C	312	4	TOTAL COUNTER 16		R	Unsigned
PARTIAL COUNTI					i.	onoighea
\$140	320	4	PARTIAL COUNTER 1	0÷10000000000	R	Unsigned
\$144	324	4	PARTIAL COUNTER 2		R	Unsigned
\$148	328	4	PARTIAL COUNTER 3		R	Unsigned
\$14C	332	4	PARTIAL COUNTER 4		R	Unsigned
\$150 \$154	336 340	4	PARTIAL COUNTER 5 PARTIAL COUNTER 6		R R	Unsigned Unsigned
<u>\$154</u> \$158	340	4	PARTIAL COUNTER 6 PARTIAL COUNTER 7		R	Unsigned
\$15C	348	4	PARTIAL COUNTER 8	1	R	Unsigned
\$160	352	4	PARTIAL COUNTER 9		R	Unsigned
\$164	356	4	PARTIAL COUNTER 10		R	Unsigned
\$168	360	4	PARTIAL COUNTER 11		R	Unsigned
\$16C	364	4	PARTIAL COUNTER 12		R	Unsigned
\$170	368	4	PARTIAL COUNTER 13		R	Unsigned
<u>\$174</u> \$178	372 376	4	PARTIAL COUNTER 14 PARTIAL COUNTER 15		R R	Unsigned Unsigned
\$170 \$17C	380	4	PARTIAL COUNTER 16		R	Unsigned
DERIVATE COUN		·				eneignea
\$180	384	4	DERIVATE COUNTER 1	0÷10000000000	R	Unsigned
\$184	388	4	DERIVATE COUNTER 2		R	Unsigned
\$188 \$18C	392 396	4	DERIVATE COUNTER 3 DERIVATE COUNTER 4		R R	Unsigned Unsigned
\$190	400	4	DERIVATE COUNTER 5		R	Unsigned
\$194	400	4	DERIVATE COUNTER 6		R	Unsigned
\$198	408	4	DERIVATE COUNTER 7		R	Unsigned
\$19C	412	4	DERIVATE COUNTER 8		R	Unsigned
\$1A0	416	4	DERIVATE COUNTER 9		R	Unsigned
\$1A4	420	4	DERIVATE COUNTER 10		R	Unsigned
\$1A8 \$1AC	424 428	4	DERIVATE COUNTER 11 DERIVATE COUNTER 12		R R	Unsigned Unsigned
\$1B0	432	4	DERIVATE COUNTER 12 DERIVATE COUNTER 13		R	Unsigned
\$1B4	436	4	DERIVATE COUNTER 14		R	Unsigned
\$1B8	440	4	DERIVATE COUNTER 15		R	Unsigned
\$1BC	444	4	DERIVATE COUNTER 16		R	Unsigned
BOOLEAN VALUE		0		0.4	5	11
\$1C0 \$1C2	448 450	2	BOOLEAN VALUE 1 BOOLEAN VALUE 2	0÷1	R R	Unsigned Unsigned
\$1C2 \$1C4	450	2	BOOLEAN VALUE 3		R	Unsigned
\$1C4	454	2	BOOLEAN VALUE 4	1	R	Unsigned
\$1C8	456	2	BOOLEAN VALUE 5		R	Unsigned
\$1CA	458	2	BOOLEAN VALUE 6		R	Unsigned
\$1CC	460	2	BOOLEAN VALUE 7		R	Unsigned
\$1CE	462	2	BOOLEAN VALUE 8		R	Unsigned
\$1D0 \$1D2	464 466	2	BOOLEAN VALUE 9		R R	Unsigned
\$1D2 \$1D4	468	2	BOOLEAN VALUE 10 BOOLEAN VALUE 11		R	Unsigned Unsigned
\$1D4 \$1D6	400	2	BOOLEAN VALUE 12		R	Unsigned
	472	2	BOOLEAN VALUE 13	1	R	Unsigned
\$1D8	712			İ		Unsigned
\$1DA	474	2	BOOLEAN VALUE 14		R	Unsigned
\$1DA \$1DC	474 476	2 2	BOOLEAN VALUE 15		R	Unsigned
\$1DA \$1DC \$1DE	474 476 478	2				Ŭ
\$1DA \$1DC	474 476 478	2 2	BOOLEAN VALUE 15	-1000000000	R	Unsigned

\$204	516	4	MATHEMATICS COUNTER 2	1	R	Unsigned
\$208	520	4	MATHEMATICS COUNTER 3		R	Unsigned
\$20C	524	4	MATHEMATICS COUNTER 4		R	Unsigned
\$210	528	4	MATHEMATICS COUNTER 5		R	Unsigned
\$214	532	4	MATHEMATICS COUNTER 6		R	Unsigned
\$218	536	4	MATHEMATICS COUNTER 7		R	Unsigned
\$21C	540	4	MATHEMATICS COUNTER 8		R	Unsigned
\$220	542	4	MATHEMATICS COUNTER 9		R	Unsigned
\$224	546	4	MATHEMATICS COUNTER 10		R	Unsigned
\$228	550	4	MATHEMATICS COUNTER 11		R	Unsigned
\$22C	554	4	MATHEMATICS COUNTER 12		R	Unsigned
\$230	558	4	MATHEMATICS COUNTER 13		R	Unsigned
\$234	562	4	MATHEMATICS COUNTER 14		R	Unsigned
\$238	566	4	MATHEMATICS COUNTER 15		R	Unsigned
\$23C Alarm Status	570	4	MATHEMATICS COUNTER 16		R	Unsigned
\$300	768	2	ALARM STATUS 1	0 = OFF 1 = ON	R	Unsigned
\$302	770	2	ALARM STATUS 2	T = ON	R	Unsigned
\$304	772	2	ALARM STATUS 3		R	Unsigned
\$306	774	2	ALARM STATUS 4		R	Unsigned
\$308	776	2	ALARM STATUS 5		R	Unsigned
\$30A	778	2	ALARM STATUS 6		R	Unsigned
\$30C	780	2	ALARM STATUS 7		R	Unsigned
\$30E	782	2	ALARM STATUS 8		R	Unsigned
\$310	784	2	ALARM STATUS 9		R	Unsigned
\$312	786	2	ALARM STATUS 10		R	Unsigned
\$314	788	2	ALARM STATUS 11		R	Unsigned
\$316	790	2	ALARM STATUS 12		R	Unsigned
\$318	792	2	ALARM STATUS 13		R	Unsigned
\$31A	794	2	ALARM STATUS 14		R	Unsigned
\$31C	796	2	ALARM STATUS 15		R	Unsigned
\$31E	798	2	ALARM STATUS 16		R	Unsigned
ACKNOLEDGED A	LARM					
\$320	800	2	ACKNOLEDGED ALARMS	0 = All alarms ack 1 = Almost one alarm	R	Unsigned
\$322	802	2	NOT ACKNOLEDGED ALARMS NUMBER	not ack 0÷16	R	Unsigned
		4	TOTAL TIMEDAND & COUNTEDO &	0. 100000000000	P	
\$400	1024	4	TOTAL TIMEBAND 1 COUNTERS 1	0÷10000000000	R	Unsigned
\$404	1028	4	TOTAL TIMEBAND 1 COUNTERS 2		R	Unsigned
\$408	1032	4	TOTAL TIMEBAND 1 COUNTERS 3		R	Unsigned
\$40C	1036	4	TOTAL TIMEBAND 1 COUNTERS 4		R	Unsigned
\$410	1040	4	TOTAL TIMEBAND 1 COUNTERS 5		R	Unsigned
\$414	1044	4	TOTAL TIMEBAND 1 COUNTERS 6 TOTAL TIMEBAND 1 COUNTERS 7		R R	Unsigned
¢110	1049					Unsigned
\$418	1048					امم منه ا
\$41C	1052	4	TOTAL TIMEBAND 1 COUNTERS 8		R	Unsigned
\$41C \$420	1052 1056	4 4	TOTAL TIMEBAND 1 COUNTERS 9		R R	Unsigned
\$41C \$420 \$424	1052 1056 1060	4 4 4	TOTAL TIMEBAND 1 COUNTERS 9 TOTAL TIMEBAND 1 COUNTERS 10		R R R	Unsigned Unsigned
\$41C \$420 \$424 \$428	1052 1056 1060 1064	4 4 4 4	TOTAL TIMEBAND 1 COUNTERS 9 TOTAL TIMEBAND 1 COUNTERS 10 TOTAL TIMEBAND 1 COUNTERS 11		R R R R	Unsigned Unsigned Unsigned
\$41C \$420 \$424 \$428 \$428 \$42C	1052 1056 1060 1064 1068	4 4 4 4 4 4	TOTAL TIMEBAND 1 COUNTERS 9 TOTAL TIMEBAND 1 COUNTERS 10 TOTAL TIMEBAND 1 COUNTERS 11 TOTAL TIMEBAND 1 COUNTERS 12		R R R R R	Unsigned Unsigned Unsigned Unsigned
\$41C \$420 \$424 \$428 \$42C \$430	1052 1056 1060 1064 1068 1072	4 4 4 4 4 4 4	TOTAL TIMEBAND 1 COUNTERS 9 TOTAL TIMEBAND 1 COUNTERS 10 TOTAL TIMEBAND 1 COUNTERS 11 TOTAL TIMEBAND 1 COUNTERS 12 TOTAL TIMEBAND 1 COUNTERS 13		R R R R R R	Unsigned Unsigned Unsigned Unsigned
\$41C \$420 \$424 \$428 \$42C \$430 \$434	1052 1056 1060 1064 1068 1072 1076	4 4 4 4 4 4 4 4	TOTAL TIMEBAND 1 COUNTERS 9 TOTAL TIMEBAND 1 COUNTERS 10 TOTAL TIMEBAND 1 COUNTERS 11 TOTAL TIMEBAND 1 COUNTERS 12 TOTAL TIMEBAND 1 COUNTERS 13 TOTAL TIMEBAND 1 COUNTERS 14		R R R R R R R	Unsigned Unsigned Unsigned Unsigned Unsigned
\$41C \$420 \$424 \$428 \$42C \$430 \$434 \$438	1052 1056 1060 1064 1068 1072 1076 1080	4 4 4 4 4 4 4 4 4	TOTAL TIMEBAND 1 COUNTERS 9 TOTAL TIMEBAND 1 COUNTERS 10 TOTAL TIMEBAND 1 COUNTERS 11 TOTAL TIMEBAND 1 COUNTERS 12 TOTAL TIMEBAND 1 COUNTERS 13 TOTAL TIMEBAND 1 COUNTERS 14 TOTAL TIMEBAND 1 COUNTERS 15		R R R R R R R R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$41C \$420 \$424 \$428 \$42C \$430 \$434	1052 1056 1060 1064 1068 1072 1076 1080 1084	4 4 4 4 4 4 4 4	TOTAL TIMEBAND 1 COUNTERS 9 TOTAL TIMEBAND 1 COUNTERS 10 TOTAL TIMEBAND 1 COUNTERS 11 TOTAL TIMEBAND 1 COUNTERS 12 TOTAL TIMEBAND 1 COUNTERS 13 TOTAL TIMEBAND 1 COUNTERS 14		R R R R R R R	Unsigned Unsigned Unsigned Unsigned Unsigned
\$41C \$420 \$424 \$428 \$42C \$430 \$434 \$438 \$438	1052 1056 1060 1064 1068 1072 1076 1080 1084	4 4 4 4 4 4 4 4 4	TOTAL TIMEBAND 1 COUNTERS 9 TOTAL TIMEBAND 1 COUNTERS 10 TOTAL TIMEBAND 1 COUNTERS 11 TOTAL TIMEBAND 1 COUNTERS 12 TOTAL TIMEBAND 1 COUNTERS 13 TOTAL TIMEBAND 1 COUNTERS 14 TOTAL TIMEBAND 1 COUNTERS 15 TOTAL TIMEBAND 1 COUNTERS 16	0÷1000000000	R R R R R R R R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$41C \$420 \$424 \$428 \$42C \$430 \$434 \$438 \$438 \$43C TOTAL TIMEBAND	1052 1056 1060 1064 1068 1072 1076 1080 1084 2 COUNTERS	4 4 4 4 4 4 4 4 4 4 4	TOTAL TIMEBAND 1 COUNTERS 9 TOTAL TIMEBAND 1 COUNTERS 10 TOTAL TIMEBAND 1 COUNTERS 11 TOTAL TIMEBAND 1 COUNTERS 12 TOTAL TIMEBAND 1 COUNTERS 13 TOTAL TIMEBAND 1 COUNTERS 14 TOTAL TIMEBAND 1 COUNTERS 15	0÷1000000000	R R R R R R R R R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$41C \$420 \$424 \$428 \$42C \$430 \$434 \$438 \$438 \$43C TOTAL TIMEBAND \$440	1052 1056 1060 1064 1068 1072 1076 1080 1084 2 COUNTERS 1088	4 4 4 4 4 4 4 4 4 4 4	TOTAL TIMEBAND 1 COUNTERS 9 TOTAL TIMEBAND 1 COUNTERS 10 TOTAL TIMEBAND 1 COUNTERS 11 TOTAL TIMEBAND 1 COUNTERS 12 TOTAL TIMEBAND 1 COUNTERS 13 TOTAL TIMEBAND 1 COUNTERS 14 TOTAL TIMEBAND 1 COUNTERS 15 TOTAL TIMEBAND 1 COUNTERS 16 TOTAL TIMEBAND 2 COUNTERS 1	0÷1000000000	R R R R R R R R R R R R R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$41C \$420 \$424 \$428 \$42C \$430 \$434 \$438 \$438 \$43C TOTAL TIMEBAND \$440 \$444	1052 1056 1060 1064 1068 1072 1076 1080 1084 2 COUNTERS 1088 1092	$ \begin{array}{c} 4 \\ 4 \\ 4 \\ 4 \\ 4 \\ 4 \\ 4 \\ 4 \\ 4 \\ 4 \\$	TOTAL TIMEBAND 1 COUNTERS 9 TOTAL TIMEBAND 1 COUNTERS 10 TOTAL TIMEBAND 1 COUNTERS 11 TOTAL TIMEBAND 1 COUNTERS 12 TOTAL TIMEBAND 1 COUNTERS 13 TOTAL TIMEBAND 1 COUNTERS 14 TOTAL TIMEBAND 1 COUNTERS 15 TOTAL TIMEBAND 1 COUNTERS 16 TOTAL TIMEBAND 2 COUNTERS 1 TOTAL TIMEBAND 2 COUNTERS 2	0÷1000000000	R R R R R R R R R R R R R R R R R R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$41C \$420 \$424 \$428 \$42C \$430 \$434 \$438 \$436 TOTAL TIMEBAND \$440 \$444 \$448 \$448	1052 1056 1060 1064 1068 1072 1076 1080 1084 2 COUNTERS 1088 1092 1096	4 4 4 4 4 4 4 4 4 4 4 4 4 4	TOTAL TIMEBAND 1 COUNTERS 9 TOTAL TIMEBAND 1 COUNTERS 10 TOTAL TIMEBAND 1 COUNTERS 11 TOTAL TIMEBAND 1 COUNTERS 12 TOTAL TIMEBAND 1 COUNTERS 13 TOTAL TIMEBAND 1 COUNTERS 14 TOTAL TIMEBAND 1 COUNTERS 15 TOTAL TIMEBAND 1 COUNTERS 16 TOTAL TIMEBAND 2 COUNTERS 1 TOTAL TIMEBAND 2 COUNTERS 2 TOTAL TIMEBAND 2 COUNTERS 3	0÷1000000000	R R R R R R R R R R R R R R R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$41C \$420 \$424 \$428 \$42C \$430 \$434 \$438 \$438 \$43C TOTAL TIMEBAND \$440 \$444 \$448	1052 1056 1060 1064 1068 1072 1076 1080 1084 2 COUNTERS 1088 1092 1096 1100 1104	$ \begin{array}{c} 4 \\ 4 \\ 4 \\ 4 \\ 4 \\ 4 \\ 4 \\ 4 \\ 4 \\ 4 \\$	TOTAL TIMEBAND 1 COUNTERS 9 TOTAL TIMEBAND 1 COUNTERS 10 TOTAL TIMEBAND 1 COUNTERS 11 TOTAL TIMEBAND 1 COUNTERS 12 TOTAL TIMEBAND 1 COUNTERS 13 TOTAL TIMEBAND 1 COUNTERS 14 TOTAL TIMEBAND 1 COUNTERS 15 TOTAL TIMEBAND 1 COUNTERS 16 TOTAL TIMEBAND 2 COUNTERS 1 TOTAL TIMEBAND 2 COUNTERS 2 TOTAL TIMEBAND 2 COUNTERS 3 TOTAL TIMEBAND 2 COUNTERS 4 TOTAL TIMEBAND 2 COUNTERS 4 TOTAL TIMEBAND 2 COUNTERS 5	0÷1000000000	R R R R R R R R R R R R R R R R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
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| \$4C0 | 1216 | 4 | TOTAL TIMEBAND 4 COUNTERS 1 | 0÷100000000000 | R

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| \$4C4 | 1220 | 4 | TOTAL TIMEBAND 4 COUNTERS 2 | | R

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| \$4C8 | 1224 | 4 | TOTAL TIMEBAND 4 COUNTERS 3 | | R

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| \$4CC | 1228 | 4 | TOTAL TIMEBAND 4 COUNTERS 4 | | R

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| \$4D0 | 1232 | 4 | TOTAL TIMEBAND 4 COUNTERS 5 | | R

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Data Concentrator-EML16 communication Protocol

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\$5C0	1472	4	PARTIAL TIMEBAND 4 COUNTERS 1	0÷10000000000	R	Unsigned
\$5C4	1476	4	PARTIAL TIMEBAND 4 COUNTERS 2		R	Unsigned
\$5C8	1480	4	PARTIAL TIMEBAND 4 COUNTERS 3		R	Unsigned
\$5CC	1484	4	PARTIAL TIMEBAND 4 COUNTERS 4		R	Unsigned
\$5D0	1488	4	PARTIAL TIMEBAND 4 COUNTERS 5		R	Unsigned
\$5D4	1492	4	PARTIAL TIMEBAND 4 COUNTERS 6		R	Unsigned
\$5D8	1496	4	PARTIAL TIMEBAND 4 COUNTERS 7		R	Unsigned
\$5DC	1500	4	PARTIAL TIMEBAND 4 COUNTERS 8		R	Unsigned
\$5E0	1504	4	PARTIAL TIMEBAND 4 COUNTERS 9		R	Unsigned
\$5E4	1508	4	PARTIAL TIMEBAND 4 COUNTERS 10		R	Unsigned
\$5E8	1512	4	PARTIAL TIMEBAND 4 COUNTERS 11		R	Unsigned
\$5EC	1516	4	PARTIAL TIMEBAND 4 COUNTERS 12		R	Unsigned
\$5F0	1520	4	PARTIAL TIMEBAND 4 COUNTERS 13		R	Unsigned
\$5F4	1524	4	PARTIAL TIMEBAND 4 COUNTERS 14		R	Unsigned
\$5F8	1528	4	PARTIAL TIMEBAND 4 COUNTERS 15		R	Unsigned
\$5FC	1532	4	PARTIAL TIMEBAND 4 COUNTERS 16		R	Unsigned
MEMORY RATED		S				Cheighea
\$600	1536	4	MEMORY RATED TOTAL COUNTERS 1	0÷100000000000	R	Unsigned
\$604	1540	4	MEMORY RATED TOTAL COUNTERS 2		R	Unsigned
\$608	1544	4	MEMORY RATED TOTAL COUNTERS 3		R	Unsigned
\$60C	1548	4	MEMORY RATED TOTAL COUNTERS 4		R	Unsigned
\$610	1552	4	MEMORY RATED TOTAL COUNTERS 5		R	Unsigned
\$614	1556	4	MEMORY RATED TOTAL COUNTERS 6		R	Unsigned
\$618	1560	4	MEMORY RATED TOTAL COUNTERS 7		R	Unsigned
\$61C	1564	4	MEMORY RATED TOTAL COUNTERS 8		R	Unsigned
\$620	1568	4	MEMORY RATED TOTAL COUNTERS 9		R	Unsigned
\$624	1572	4	MEMORY RATED TOTAL COUNTERS 10		R	Unsigned
\$628	1576	4	MEMORY RATED TOTAL COUNTERS 11		R	Unsigned
\$62C	1580	4	MEMORY RATED TOTAL COUNTERS 12		R	Unsigned
\$630	1584	4	MEMORY RATED TOTAL COUNTERS 13		R	Unsigned
\$634	1588	4	MEMORY RATED TOTAL COUNTERS 14		R	Unsigned
\$638	1592	4	MEMORY RATED TOTAL COUNTERS 15		R	Unsigned
\$63C	1596	4	MEMORY RATED TOTAL COUNTERS 16		R	Unsigned
MEMORY RATED	PARTIAL COUNTE	RS				
\$640	1600	4	MEMORY RATED PARTIAL COUNTERS 1	0÷10000000000	R	Unsigned
\$644	1604	4	MEMORY RATED PARTIAL COUNTERS 2		R	Unsigned
\$648	1608	4	MEMORY RATED PARTIAL COUNTERS 3		R	Unsigned
\$64C	1612	4	MEMORY RATED PARTIAL COUNTERS 4		R	Unsigned
\$650	1616	4	MEMORY RATED PARTIAL COUNTERS 5		R	Unsigned
\$654	1620	4	MEMORY RATED PARTIAL COUNTERS 6		R	Unsigned
\$658	1624	4	MEMORY RATED PARTIAL COUNTERS 7		R	Unsigned
\$65C	1628	4	MEMORY RATED PARTIAL COUNTERS 8		R	Unsigned
\$660	1632	4	MEMORY RATED PARTIAL COUNTERS 9		R	Unsigned
\$664	1636	4	MEMORY RATED PARTIAL COUNTERS 10		R	Unsigned
\$668	1640	4	MEMORY RATED PARTIAL COUNTERS 11		R	Unsigned
\$66C	1644	4	MEMORY RATED PARTIAL COUNTERS 12		R	Unsigned
\$670	1648	4	MEMORY RATED PARTIAL COUNTERS 13		R	Unsigned
\$674	1652	4	MEMORY RATED PARTIAL COUNTERS 14		R	Unsigned
\$678	1656	4	MEMORY RATED PARTIAL COUNTERS 15		R	Unsigned
\$67C	1660	4	MEMORY RATED PARTIAL COUNTERS 16		R	Unsigned
DIGITAL OUTPUT	STATUS					
\$720	1824	2	DO1 STATUS	0 = OFF 1 = ON	R	Unsigned
\$722	1826	2	DO2 STATUS	0 = OFF 1 = ON	R	Unsigned

Device Parameters

It is possible to send a broadcast command (Address Field equal 0x00) for all write command.

Warning: All Write command could be send in Broadcast Mode (Modbus Node ID 0) but if the Modbus Register or Modbus Parameters is wrong anything messages are returned.

Register HEX	Register DEC	Word	Description	Value	W/R	Туре
			GENERAL			
LANGUAGE		_		1 = English (def.)		
\$1000	4096	2	LANGUAGE	2 = Italian	W/R	Unsigned
TIME \$1002	4098	2	HOUR	0÷23 (default 12)	W/R	Unsigned
\$1004	4100	2	MINUTE	0÷59 (default 0)	W/R	Unsigned
\$1006	4102	2	SECOND	0÷59 (default 0)	W/R	Unsigned
DATE \$1008 \$100A	4104	2	WEK-DAY DAY	0 = Sunday 1 = Monday (def.) 2 = Tuesday 3 = Wednesday 4 = Thursday 5 = Friday 6 = Saturday 1 (default)÷31	W/R	Unsigned
\$100A \$100C	4108	2	MONTH	1 (default)÷12	W/R	Unsigned
\$100E	4110	2	YEAR	19 (default)÷250	W/R	Unsigned
BUZZER \$1010	4012	2	BUZZER	0 = OFF (def.) 1 = ON	W/R	Unsigned
			COUNTERS			
VISUALIZATION				0 = OFF		
\$1100	4111	2	VISUALIZATION COUNTER 1	1 = ON (def.)	W/R	Unsigned
<u>\$1102</u> \$1104	4112 4113	2	VISUALIZATION COUNTER 2 VISUALIZATION COUNTER 3		W/R W/R	Unsigned Unsigned
\$1104 \$1106	4113	2	VISUALIZATION COUNTER 3		W/R	Unsigned
\$1108	4115	2	VISUALIZATION COUNTER 5		W/R	Unsigned
\$110A	4116	2	VISUALIZATION COUNTER 6		W/R	Unsigned
\$110C	4117	2	VISUALIZATION COUNTER 7		W/R	Unsigned
\$110E	4118	2	VISUALIZATION COUNTER 8		W/R	Unsigned
\$1110	4119	2	VISUALIZATION COUNTER 9		W/R	Unsigned
\$1112	4120	2	VISUALIZATION COUNTER 10		W/R	Unsigned
\$1114	4121	2	VISUALIZATION COUNTER 11		W/R	Unsigned
\$1116	4122	2	VISUALIZATION COUNTER 12		W/R	Unsigned
\$1118	4123	2	VISUALIZATION COUNTER 13		W/R	Unsigned
\$111A	4124	2	VISUALIZATION COUNTER 14		W/R	Unsigned
\$111C	4125	2	VISUALIZATION COUNTER 15		W/R	Unsigned
\$111E DESCRIPTION	4126	2	VISUALIZATION COUNTER 16		W/R	Unsigned
\$1120	4384	10	DESCRIPTION COUNTER 1	Up to 20 characters (\$20÷\$7A) (def.: Counter 01)	W/R	Unsigned
\$112A	4394	10	DESCRIPTION COUNTER 2	(def.: Counter 02)	W/R	Unsigned
\$1134	4404	10	DESCRIPTION COUNTER 3	(def.: Counter 03)	W/R	Unsigned
\$113E	4414	10	DESCRIPTION COUNTER 4	(def.: Counter 04)	W/R	Unsigned
\$1148	4424	10	DESCRIPTION COUNTER 5	(def.: Counter 05)	W/R	Unsigned
\$1152	4434	10	DESCRIPTION COUNTER 6	(def.: Counter 06)	W/R	Unsigned
\$115C	4444 4454	10	DESCRIPTION COUNTER 7	(def.: Counter 07) (def.: Counter 08)	W/R W/R	Unsigned
<u>\$1166</u> \$1170	4454 4464	10 10	DESCRIPTION COUNTER 8 DESCRIPTION COUNTER 9	(def.: Counter 08) (def.: Counter 09)	W/R W/R	Unsigned Unsigned
\$117A	4464	10	DESCRIPTION COUNTER 9 DESCRIPTION COUNTER 10	(def.: Counter 10)	W/R	Unsigned
\$1184	4484	10	DESCRIPTION COUNTER 11	(def.: Counter 10)	W/R	Unsigned
\$118E	4494	10	DESCRIPTION COUNTER 12	(def.: Counter 12)	W/R	Unsigned
\$1198	4504	10	DESCRIPTION COUNTER 13	(def.: Counter 13)	W/R	Unsigned
\$11A2	4514	10	DESCRIPTION COUNTER 14	(def.: Counter 14)	W/R	Unsigned
\$11AC	4524	10	DESCRIPTION COUNTER 15	(def.: Counter 15)	W/R	Unsigned
\$11B6	4534	10	DESCRIPTION COUNTER 16	(def.: Counter 16)	W/R	Unsigned
MEASURE UNIT					-	
\$11C0	4544	3	M.U. COUNTER 1	Up to 6 characters (\$20÷\$7A) (default: kWh+)	W/R	Unsigned
\$11C3	4547	3	M.U. COUNTER 2		W/R	Unsigned
\$11C6	4550	3	M.U. COUNTER 3		W/R	Unsigned
\$11C9	4553	3	M.U. COUNTER 4		W/R	Unsigned
\$11CC	4556	3	M.U. COUNTER 5		W/R	Unsigned
\$11CF	4559	3	M.U. COUNTER 6		W/R	Unsigned
\$11D2	4562	3	M.U. COUNTER 7		W/R	Unsigned
\$11D5	4565	3	M.U. COUNTER 8		W/R	Unsigned

Data Concentrator-EML16 communication Protocol

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\$11D8	4568	3	M.U. COUNTER 9	I	W/R	Unsigned
\$11DB	4571	3	M.U. COUNTER 10		W/R	Unsigned
\$11DE	4574	3	M.U. COUNTER 11		W/R	Unsigned
\$11E1	4577	3	M.U. COUNTER 12		W/R	Unsigned
\$11E4	4580	3	M.U. COUNTER 13		W/R	Unsigned
\$11E7	4583	3	M.U. COUNTER 14		W/R	Unsigned
\$11EA	4586	3	M.U. COUNTER 15		W/R	Unsigned
\$11ED	4589	3	M.U. COUNTER 16		W/R	Unsigned
SOURCE \$11F0	4592	2	SOURCE COUNTER 1	0 = OFF (default) 1÷16 = Input 1÷16 17÷32 = Alarm 1÷16 33÷48 = Bool 1÷16	W/R	Unsigned
\$11F2	4594	2	SOURCE COUNTER 2		W/R	Unsigned
\$11F4	4596	2	SOURCE COUNTER 3		W/R	Unsigned
\$11F6	4598	2	SOURCE COUNTER 4		W/R	Unsigned
\$11F8	4600	2	SOURCE COUNTER 5		W/R	Unsigned
\$11FA	4602	2	SOURCE COUNTER 6		W/R	Unsigned
\$11FC	4604	2	SOURCE COUNTER 7		W/R	Unsigned
\$11FE	4606	2	SOURCE COUNTER 8		W/R	Unsigned
\$1200 \$1202	4608 4610	2	SOURCE COUNTER 9		W/R W/R	Unsigned
\$1202	4610	2	SOURCE COUNTER 10 SOURCE COUNTER 11		W/R	Unsigned Unsigned
\$1204	4614	2	SOURCE COUNTER 12		W/R	Unsigned
\$1208	4614	2	SOURCE COUNTER 12		W/R	Unsigned
\$1208	4618	2	SOURCE COUNTER 14		W/R	Unsigned
\$120C	4620	2	SOURCE COUNTER 15		W/R	Unsigned
\$120E	4622	2	SOURCE COUNTER 16		W/R	Unsigned
IULTIPLIER						
\$1210	4624	2	MULTIPLIER COUNTER 1	1 (default)÷1000	W/R	Unsigned
\$1212	4626	2	MULTIPLIER COUNTER 2		W/R	Unsigned
\$1214	4628	2	MULTIPLIER COUNTER 3		W/R	Unsigned
\$1216	4630	2	MULTIPLIER COUNTER 4		W/R	Unsigned
\$1218	4632	2	MULTIPLIER COUNTER 5		W/R	Unsigned
\$121A	4634	2	MULTIPLIER COUNTER 6		W/R	Unsigned
\$121C	4636	2	MULTIPLIER COUNTER 7		W/R	Unsigned
\$121E	4638	2	MULTIPLIER COUNTER 8		W/R	Unsigned
\$1220	4640	2	MULTIPLIER COUNTER 9		W/R	Unsigned
\$1222 \$1224	4642 4644	2	MULTIPLIER COUNTER 10		W/R W/R	Unsigned
\$1224 \$1226	4646	2	MULTIPLIER COUNTER 11 MULTIPLIER COUNTER 12		W/R	Unsigned Unsigned
\$1228	4648	2	MULTIPLIER COUNTER 12		W/R	Unsigned
\$1220	4650	2	MULTIPLIER COUNTER 14		W/R	Unsigned
\$122C	4652	2	MULTIPLIER COUNTER 15		W/R	Unsigned
\$122E	4654	2	MULTIPLIER COUNTER 16		W/R	Unsigned
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\$1230	4656	2	DIVIDER COUNTER 1	1 (default)÷1000	W/R	Unsigned
\$1232	4658	2	DIVIDER COUNTER 2	· · · · ·	W/R	Unsigned
\$1234	4660	2	DIVIDER COUNTER 3		W/R	Unsigned
\$1236	4662	2	DIVIDER COUNTER 4		W/R	Unsigned
\$1238	4664	2	DIVIDER COUNTER 5		W/R	Unsigned
\$123A	4666	2	DIVIDER COUNTER 6		W/R	Unsigned
\$123C	4668	2	DIVIDER COUNTER 7		W/R	Unsigned
\$123E	4670	2	DIVIDER COUNTER 8		W/R	Unsigned
\$1240	4672	2	DIVIDER COUNTER 9		W/R	Unsigned
\$1242 \$1244	4674	2	DIVIDER COUNTER 10 DIVIDER COUNTER 11		W/R W/R	Unsigned
\$1244 \$1246	4676 4678	2	DIVIDER COUNTER 11 DIVIDER COUNTER 12		W/R	Unsigned Unsigned
\$1246	4678	2	DIVIDER COUNTER 12 DIVIDER COUNTER 13		W/R	Unsigned
\$1248 \$124A	4682	2	DIVIDER COUNTER 13		W/R	Unsigned
\$124C	4684	2	DIVIDER COUNTER 15		W/R	Unsigned
\$124E	4686	2	DIVIDER COUNTER 16		W/R	Unsigned
ERIVATE TIME		_				energine a
\$1250	4688	2	DERIVATE TIME COUNTER 1	0 = OFF (def) 1÷60 (minute)	W/R	Unsigned
	1000	-	DERIVATE TIME COUNTER 2		W/R	Unsigned
\$1252	4690	2			W/R	Unsigned
\$1252 \$1254	4690 4692	2	DERIVATE TIME COUNTER 3			
\$1254 \$1256	4692 4694		DERIVATE TIME COUNTER 4		W/R	0
\$1254 \$1256 \$1258	4692 4694 4696	2 2 2	DERIVATE TIME COUNTER 4 DERIVATE TIME COUNTER 5		W/R W/R	Unsigned Unsigned
\$1254 \$1256 \$1258 \$125A	4692 4694 4696 4698	2 2 2 2	DERIVATE TIME COUNTER 4 DERIVATE TIME COUNTER 5 DERIVATE TIME COUNTER 6		W/R W/R W/R	Unsigned Unsigned Unsigned
\$1254 \$1256 \$1258 \$125A \$125A \$125C	4692 4694 4696 4698 4700	2 2 2 2 2 2	DERIVATE TIME COUNTER 4 DERIVATE TIME COUNTER 5 DERIVATE TIME COUNTER 6 DERIVATE TIME COUNTER 7		W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned
\$1254 \$1256 \$1258 \$125A \$125A \$125C \$125E	4692 4694 4696 4698 4700 4702	2 2 2 2 2 2 2 2 2	DERIVATE TIME COUNTER 4 DERIVATE TIME COUNTER 5 DERIVATE TIME COUNTER 6 DERIVATE TIME COUNTER 7 DERIVATE TIME COUNTER 8		W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned
\$1254 \$1256 \$1258 \$125A \$125C \$125C \$125E \$1260	4692 4694 4696 4698 4700 4702 4704	2 2 2 2 2 2 2 2 2 2 2	DERIVATE TIME COUNTER 4 DERIVATE TIME COUNTER 5 DERIVATE TIME COUNTER 6 DERIVATE TIME COUNTER 7 DERIVATE TIME COUNTER 8 DERIVATE TIME COUNTER 9		W/R W/R W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$1254 \$1256 \$1258 \$125A \$125C \$125C \$125E \$1260 \$1262	4692 4694 4696 4698 4700 4702 4704 4706	2 2 2 2 2 2 2 2 2 2 2 2 2	DERIVATE TIME COUNTER 4 DERIVATE TIME COUNTER 5 DERIVATE TIME COUNTER 6 DERIVATE TIME COUNTER 7 DERIVATE TIME COUNTER 8 DERIVATE TIME COUNTER 9 DERIVATE TIME COUNTER 10		W/R W/R W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$1254 \$1256 \$1258 \$125A \$125C \$125E \$1260 \$1262 \$1264	4692 4694 4696 4698 4700 4702 4704 4704 4706 4708	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	DERIVATE TIME COUNTER 4 DERIVATE TIME COUNTER 5 DERIVATE TIME COUNTER 6 DERIVATE TIME COUNTER 7 DERIVATE TIME COUNTER 8 DERIVATE TIME COUNTER 9 DERIVATE TIME COUNTER 10 DERIVATE TIME COUNTER 11		W/R W/R W/R W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$1254 \$1256 \$1258 \$125A \$125C \$125E \$1260 \$1262 \$1264 \$1266	4692 4694 4696 4698 4700 4702 4704 4704 4706 4708 4710	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	DERIVATE TIME COUNTER 4 DERIVATE TIME COUNTER 5 DERIVATE TIME COUNTER 6 DERIVATE TIME COUNTER 7 DERIVATE TIME COUNTER 8 DERIVATE TIME COUNTER 9 DERIVATE TIME COUNTER 10 DERIVATE TIME COUNTER 11 DERIVATE TIME COUNTER 12		W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$1254 \$1256 \$1258 \$125A \$125C \$125E \$1260 \$1262 \$1264	4692 4694 4696 4698 4700 4702 4704 4704 4706 4708	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	DERIVATE TIME COUNTER 4 DERIVATE TIME COUNTER 5 DERIVATE TIME COUNTER 6 DERIVATE TIME COUNTER 7 DERIVATE TIME COUNTER 8 DERIVATE TIME COUNTER 9 DERIVATE TIME COUNTER 10 DERIVATE TIME COUNTER 11		W/R W/R W/R W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned

\$126E DERIVATE MULTIP	4718 PLIER	2	DERIVATE TIME COUNTER 16		W/R	Unsigne
\$1270	4720	2	DERIVATE MULTIPLIER COUNTER 1	1÷1000 (default 60)	W/R	Unsigne
\$1272	4722	2	DERIVATE MULTIPLIER COUNTER 2		W/R	Unsigne
\$1274	4724	2	DERIVATE MULTIPLIER COUNTER 3		W/R	Unsigne
\$1276	4726	2	DERIVATE MULTIPLIER COUNTER 4		W/R	Unsigne
\$1278	4728	2	DERIVATE MULTIPLIER COUNTER 5		W/R	Unsigne
\$127A	4730	2	DERIVATE MULTIPLIER COUNTER 6		W/R	Unsigne
\$127C	4732	2	DERIVATE MULTIPLIER COUNTER 7		W/R	Unsigne
\$127E	4734	2	DERIVATE MULTIPLIER COUNTER 8		W/R	Unsigne
\$1280	4736	2	DERIVATE MULTIPLIER COUNTER 9		W/R	Unsigne
\$1282	4738	2	DERIVATE MULTIPLIER COUNTER 10		W/R	Unsigne
\$1284	4740	2	DERIVATE MULTIPLIER COUNTER 11		W/R	Unsigne
\$1286	4742	2	DERIVATE MULTIPLIER COUNTER 12		W/R	Unsigne
\$1288	4744	2	DERIVATE MULTIPLIER COUNTER 13		W/R	Unsigne
\$1288	4746	2	DERIVATE MULTIPLIER COUNTER 14		W/R	Unsigne
\$128C	4748	2	DERIVATE MULTIPLIER COUNTER 15		W/R	Unsigne
\$128E	4750	2	DERIVATE MULTIPLIER COUNTER 16		W/R	Unsigne
		2	BERRYRE MOETH EIER OOONTER 10		VV/1	onoight
\$1290	4752	2	DERIVATE DIVIDER COUNTER 1	1 (default)÷1000	W/R	Unsigne
\$1292	4754	2	DERIVATE DIVIDER COUNTER 2	1 (deladit)+1000	W/R	Unsigne
\$1292 \$1294	4754	2	DERIVATE DIVIDER COUNTER 2		W/R	Unsigne
\$1294	4758	2	DERIVATE DIVIDER COUNTER 3		W/R	Unsigne
\$1296 \$1298	4758	2	DERIVATE DIVIDER COUNTER 4		W/R	Unsigne
\$1296 \$129A	4760	2	DERIVATE DIVIDER COUNTER 5		W/R	Unsign
\$129A \$129C	4762	2	DERIVATE DIVIDER COUNTER 6		W/R	Unsign
\$129C \$129E	4766	2	DERIVATE DIVIDER COUNTER 7		W/R	Unsign
	4768				W/R	
\$12A0 \$12A2	4768	2	DERIVATE DIVIDER COUNTER 9 DERIVATE DIVIDER COUNTER 10		W/R W/R	Unsign Unsign
	-					
\$12A4 \$12A6	4772 4774	2	DERIVATE DIVIDER COUNTER 11 DERIVATE DIVIDER COUNTER 12		W/R W/R	Unsign
						Unsign
\$12A8	4776	2	DERIVATE DIVIDER COUNTER 13		W/R	Unsign
\$12AA	4778	2	DERIVATE DIVIDER COUNTER 14		W/R	Unsign
\$12AC	4780	2	DERIVATE DIVIDER COUNTER 15		W/R	Unsign
\$12AE ERIVATE MEASU	4782	2	DERIVATE DIVIDER COUNTER 16		W/R	Unsigne
\$12B0	4784	3	DERIVATE M.U. COUNTER 1	Up to 6 characters (\$20÷\$7A) (default: kW+)	W/R	Unsigne
\$12B3	4787	3	DERIVATE M.U. COUNTER 2		W/R	Unsigne
\$12B6	4790	3	DERIVATE M.U. COUNTER 3		W/R	Unsign
\$12B9	4793	3	DERIVATE M.U. COUNTER 4		W/R	Unsign
\$12BC	4796	3	DERIVATE M.U. COUNTER 5		W/R	Unsign
\$12BF	4799	3	DERIVATE M.U. COUNTER 6		W/R	Unsign
\$12C2	4802	3	DERIVATE M.U. COUNTER 7		W/R	Unsign
\$12C5	4805	3	DERIVATE M.U. COUNTER 8		W/R	Unsign
\$12C8	4808	3	DERIVATE M.U. COUNTER 9		W/R	Unsign
\$12CB	4811	3	DERIVATE M.U. COUNTER 10		W/R	Unsign
\$12CE	4814	3	DERIVATE M.U. COUNTER 11		W/R	Unsign
\$12D1	4817	3	DERIVATE M.U. COUNTER 12		W/R	Unsign
\$12D4	4820	3	DERIVATE M.U. COUNTER 13		W/R	Unsign
\$12D7	4823	3	DERIVATE M.U. COUNTER 14		W/R	Unsign
\$12DA	4826	3	DERIVATE M.U. COUNTER 15		W/R	Unsign
\$12DD	4829	3	DERIVATE M.U. COUNTER 16		W/R	Unsign
EMORY TYPE		_	MEMORY TYPE COUNTER 1	0 = OFF (default)	W/R	Unsign
\$12E0	4832	2		1 = Total 2 = Differential		
\$12E2	4832	2	MEMORY TYPE COUNTER 2		W/R	Unsign
\$12E2 \$12E4			MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3		W/R	Unsign
\$12E2 \$12E4 \$12E6	4834 4836 4838	2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4		W/R W/R	Unsign Unsign
\$12E2 \$12E4 \$12E6 \$12E8	4834 4836 4838 4840	2 2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4 MEMORY TYPE COUNTER 5		W/R W/R W/R	Unsign Unsign Unsign
\$12E2 \$12E4 \$12E6 \$12E8 \$12EA	4834 4836 4838 4840 4842	2 2 2 2 2 2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4 MEMORY TYPE COUNTER 5 MEMORY TYPE COUNTER 6		W/R W/R W/R W/R	Unsign Unsign Unsign Unsign
\$12E2 \$12E4 \$12E6 \$12E8 \$12EA \$12EA \$12EC	4834 4836 4838 4840 4842 4844	2 2 2 2 2 2 2 2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4 MEMORY TYPE COUNTER 5 MEMORY TYPE COUNTER 6 MEMORY TYPE COUNTER 7		W/R W/R W/R W/R W/R	Unsign Unsign Unsign Unsign Unsign
\$12E2 \$12E4 \$12E6 \$12E8 \$12EA \$12EC \$12EE	4834 4836 4838 4840 4842 4844 4844	2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4 MEMORY TYPE COUNTER 5 MEMORY TYPE COUNTER 6 MEMORY TYPE COUNTER 7 MEMORY TYPE COUNTER 8		W/R W/R W/R W/R W/R W/R W/R	Unsign Unsign Unsign Unsign Unsign Unsign
\$12E2 \$12E4 \$12E6 \$12E8 \$12EA \$12EC \$12EE \$12EE \$12F0	4834 4836 4838 4840 4842 4844 4844 4846 4848	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4 MEMORY TYPE COUNTER 5 MEMORY TYPE COUNTER 6 MEMORY TYPE COUNTER 7 MEMORY TYPE COUNTER 8 MEMORY TYPE COUNTER 9		W/R W/R W/R W/R W/R W/R W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$12E2 \$12E4 \$12E6 \$12E8 \$12EA \$12EC \$12EE \$12EE \$12F0 \$12F2	4834 4836 4838 4840 4842 4844 4844 4846 4848 4850	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4 MEMORY TYPE COUNTER 5 MEMORY TYPE COUNTER 6 MEMORY TYPE COUNTER 7 MEMORY TYPE COUNTER 8 MEMORY TYPE COUNTER 9 MEMORY TYPE COUNTER 10		W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$12E2 \$12E4 \$12E6 \$12E8 \$12EA \$12EC \$12EE \$12EC \$12EE \$12F0 \$12F2 \$12F4	4834 4836 4838 4840 4842 4844 4844 4846 4848 4850 4852	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4 MEMORY TYPE COUNTER 5 MEMORY TYPE COUNTER 6 MEMORY TYPE COUNTER 7 MEMORY TYPE COUNTER 8 MEMORY TYPE COUNTER 9 MEMORY TYPE COUNTER 9 MEMORY TYPE COUNTER 10		W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$12E2 \$12E4 \$12E6 \$12E8 \$12EA \$12EC \$12EE \$12EC \$12EE \$12F0 \$12F2 \$12F2 \$12F4 \$12F6	4834 4836 4838 4840 4842 4844 4844 4846 4848 4850 4852 4854	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4 MEMORY TYPE COUNTER 5 MEMORY TYPE COUNTER 6 MEMORY TYPE COUNTER 7 MEMORY TYPE COUNTER 8 MEMORY TYPE COUNTER 9 MEMORY TYPE COUNTER 10 MEMORY TYPE COUNTER 11 MEMORY TYPE COUNTER 12		W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$12E2 \$12E4 \$12E6 \$12E8 \$12EA \$12EC \$12EE \$12E0 \$12F0 \$12F2 \$12F4	4834 4836 4838 4840 4842 4844 4844 4846 4848 4850 4852	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4 MEMORY TYPE COUNTER 5 MEMORY TYPE COUNTER 6 MEMORY TYPE COUNTER 7 MEMORY TYPE COUNTER 8 MEMORY TYPE COUNTER 9 MEMORY TYPE COUNTER 9 MEMORY TYPE COUNTER 10		W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$12E2 \$12E4 \$12E6 \$12E8 \$12EA \$12EC \$12EE \$12EC \$12EE \$12F0 \$12F2 \$12F2 \$12F4 \$12F6	4834 4836 4838 4840 4842 4844 4844 4846 4848 4850 4852 4854	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4 MEMORY TYPE COUNTER 5 MEMORY TYPE COUNTER 6 MEMORY TYPE COUNTER 7 MEMORY TYPE COUNTER 8 MEMORY TYPE COUNTER 9 MEMORY TYPE COUNTER 10 MEMORY TYPE COUNTER 11 MEMORY TYPE COUNTER 12		W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$12E2 \$12E4 \$12E6 \$12E8 \$12EA \$12EC \$12EE \$12F0 \$12F2 \$12F2 \$12F4 \$12F6 \$12F8	4834 4836 4838 4840 4842 4844 4844 4846 4848 4850 4852 4852 4854 4856	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4 MEMORY TYPE COUNTER 5 MEMORY TYPE COUNTER 6 MEMORY TYPE COUNTER 7 MEMORY TYPE COUNTER 8 MEMORY TYPE COUNTER 9 MEMORY TYPE COUNTER 10 MEMORY TYPE COUNTER 11 MEMORY TYPE COUNTER 12 MEMORY TYPE COUNTER 13		W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$12E2 \$12E4 \$12E6 \$12E8 \$12EA \$12EC \$12EE \$12F0 \$12F2 \$12F2 \$12F4 \$12F6 \$12F8 \$12F8 \$12FA	4834 4836 4838 4840 4842 4844 4844 4846 4848 4850 4852 4852 4854 4856 4858	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4 MEMORY TYPE COUNTER 5 MEMORY TYPE COUNTER 6 MEMORY TYPE COUNTER 7 MEMORY TYPE COUNTER 8 MEMORY TYPE COUNTER 9 MEMORY TYPE COUNTER 10 MEMORY TYPE COUNTER 11 MEMORY TYPE COUNTER 11 MEMORY TYPE COUNTER 12 MEMORY TYPE COUNTER 13 MEMORY TYPE COUNTER 14		W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$12E2 \$12E4 \$12E6 \$12E8 \$12EA \$12EC \$12EC \$12FC \$12F2 \$12F4 \$12F4 \$12F4 \$12F6 \$12F8 \$12F8 \$12FA \$12FA \$12FA \$12FA	4834 4836 4838 4840 4842 4844 4846 4848 4850 4852 4852 4854 4856 4858 4860	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4 MEMORY TYPE COUNTER 5 MEMORY TYPE COUNTER 6 MEMORY TYPE COUNTER 7 MEMORY TYPE COUNTER 8 MEMORY TYPE COUNTER 9 MEMORY TYPE COUNTER 10 MEMORY TYPE COUNTER 10 MEMORY TYPE COUNTER 11 MEMORY TYPE COUNTER 13 MEMORY TYPE COUNTER 13 MEMORY TYPE COUNTER 14 MEMORY TYPE COUNTER 15		W/R W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$12E2 \$12E4 \$12E6 \$12E8 \$12EA \$12EC \$12EE \$12F0 \$12F2 \$12F4 \$12F6 \$12F8 \$12F8 \$12FA \$12FA \$12FA \$12FA \$12FA \$12FC \$12FE	4834 4836 4838 4840 4842 4844 4846 4848 4850 4852 4852 4854 4856 4858 4860	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY TYPE COUNTER 2 MEMORY TYPE COUNTER 3 MEMORY TYPE COUNTER 4 MEMORY TYPE COUNTER 5 MEMORY TYPE COUNTER 6 MEMORY TYPE COUNTER 7 MEMORY TYPE COUNTER 8 MEMORY TYPE COUNTER 9 MEMORY TYPE COUNTER 10 MEMORY TYPE COUNTER 10 MEMORY TYPE COUNTER 11 MEMORY TYPE COUNTER 13 MEMORY TYPE COUNTER 13 MEMORY TYPE COUNTER 14 MEMORY TYPE COUNTER 15		W/R W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign

¢1204	4060			I		Unsigned
\$1304 \$1306	4868 4870	2	MEMORY RATE COUNTER 3 MEMORY RATE COUNTER 4		W/R W/R	Unsigned Unsigned
\$1308	4872	2	MEMORY RATE COUNTER 5		W/R	Unsigned
\$130A	4874	2	MEMORY RATE COUNTER 6		W/R	Unsigned
\$130C	4876	2	MEMORY RATE COUNTER 7		W/R	Unsigned
\$130E	4878	2	MEMORY RATE COUNTER 8		W/R	Unsigned
\$1310	4880	2	MEMORY RATE COUNTER 9		W/R	Unsigned
\$1312	4882	2	MEMORY RATE COUNTER 10		W/R	Unsigned
\$1314	4884	2	MEMORY RATE COUNTER 11		W/R	Unsigned
\$1316	4886	2	MEMORY RATE COUNTER 12		W/R	Unsigned
\$1318	4888	2	MEMORY RATE COUNTER 13		W/R	Unsigned
\$131A	4890	2	MEMORY RATE COUNTER 14		W/R	Unsigned
\$131C	4892	2	MEMORY RATE COUNTER 15		W/R	Unsigned
\$131E	4894	2	MEMORY RATE COUNTER 16		W/R	Unsigned
<i>QIOIE</i>	1001	-	ALARMS			onoignou
SOURCE \$1400	5120	2	SOURCE ALARM 1	$\begin{array}{l} 0 = OFF \mbox{ (default)} \\ 1\div 16 = Input \ 1\div 16 \\ 17\div 32 = Cnt. Tot. \ 1\div 16 \\ 33\div 48 = Cnt.Par. \ 1\div 16 \\ 49\div 64 = Derivate \ 1\div 16 \\ 65\div 80 = Bool. \ 1\div 16 \\ 81\div 96 = Math. \ 1\div 16 \end{array}$	W/R	Unsigned
\$1402	5122	2	SOURCE ALARM 2		W/R	Unsigned
\$1404	5124	2	SOURCE ALARM 3		W/R	Unsigned
\$1406	5126	2	SOURCE ALARM 4		W/R	Unsigned
\$1408	5128	2	SOURCE ALARM 5		W/R	Unsigned
\$140A	5130	2	SOURCE ALARM 6		W/R	Unsigned
\$140C	5132	2	SOURCE ALARM 7		W/R	Unsigned
\$140E	5134	2	SOURCE ALARM 8		W/R	Unsigned
\$1410	5136	2	SOURCE ALARM 9		W/R	Unsigned
\$1412	5138	2	SOURCE ALARM 10		W/R	Unsigned
\$1414	5140	2	SOURCE ALARM 11		W/R	Unsigned
\$1416	5142	2	SOURCE ALARM 12		W/R	Unsigned
\$1418	5144	2	SOURCE ALARM 13		W/R	Unsigned
\$141A	5146	2	SOURCE ALARM 14		W/R	Unsigned
\$141C	5148		SOURCE ALARM 15		W/R	Unsigned
\$141C \$141E	5150	2			W/R	
Ŧ	5150	Z	SOURCE ALARM 16		VV/K	Unsigned
DESCRIPTION				Lin to 20 characters		
\$1420	5152	10	DESCRIPTION ALARM 1	Up to 20 characters (\$20÷\$7A) (def.: Alarm 01)	W/R	Unsigned
\$142A	5162	10	DESCRIPTION ALARM 2	(def.: Alarm 02)	W/R	Unsigned
\$1434	5172	10	DESCRIPTION ALARM 3	(def.: Alarm 03)	W/R	Unsigned
\$143E	5182	10	DESCRIPTION ALARM 4	(def.: Alarm 04)	W/R	Unsigned
\$1448	5192	10	DESCRIPTION ALARM 5	(def.: Alarm 05)	W/R	Unsigned
\$1452	5202	10	DESCRIPTION ALARM 6	(def.: Alarm 06)	W/R	Unsigned
\$145C	5212	10	DESCRIPTION ALARM 7	(def.: Alarm 07)	W/R	Unsigned
\$1466	5222	10	DESCRIPTION ALARM 8	(def.: Alarm 08)	W/R	Unsigned
\$1470	5232	10	DESCRIPTION ALARM 9	(def.: Alarm 09)	W/R	Unsigned
\$147A	5242	10	DESCRIPTION ALARM 10	(def.: Alarm 10)	W/R	Unsigned
\$1484	5252	10	DESCRIPTION ALARM 11	(def.: Alarm 11)	W/R	Unsigned
\$148E	5262	10	DESCRIPTION ALARM 12	(def.: Alarm 12)	W/R	Unsigned
\$1498	5272	10	DESCRIPTION ALARM 13	(def.: Alarm 13)	W/R	Unsigned
\$1498 \$14A2	5282	10	DESCRIPTION ALARM 13 DESCRIPTION ALARM 14	(def.: Alarm 13)	W/R	Unsigned
\$14A2 \$14AC	5292	10	DESCRIPTION ALARM 14 DESCRIPTION ALARM 15	(def.: Alarm 15)	W/R	Unsigned
\$14B6	5302	10	DESCRIPTION ALARM 16	(def.: Alarm 16)	W/R	Unsigned
MEMORY	0002	10			v V / IN	Unaighed
\$14C0	5312	2	MEMORY ALARM 1	0 = OFF (default) 1 = ON	W/R	Unsigned
\$14C2	5314	2	MEMORY ALARM 2		W/R	Unsigned
\$14C4	5316	2	MEMORY ALARM 3		W/R	Unsigned
\$14C6	5318	2	MEMORY ALARM 4		W/R	Unsigned
				1	W/R	Unsigned
\$14C8	5320	2	MEMORY ALARM 5		VV/r	
\$14C8 \$14CA	5320 5322	2	MEMORY ALARM 5 MEMORY ALARM 6			Unsigned
\$14CA	5322	2	MEMORY ALARM 6		W/R	Unsigned Unsigned
\$14CA \$14CC	5322 5324	2 2	MEMORY ALARM 6 MEMORY ALARM 7		W/R W/R	Unsigned
\$14CA \$14CC \$14CE	5322 5324 5326	2 2 2	MEMORY ALARM 6 MEMORY ALARM 7 MEMORY ALARM 8		W/R W/R W/R	Unsigned Unsigned
\$14CA \$14CC \$14CE \$14D0	5322 5324 5326 5328	2 2 2 2 2	MEMORY ALARM 6 MEMORY ALARM 7 MEMORY ALARM 8 MEMORY ALARM 9		W/R W/R W/R W/R	Unsigned Unsigned Unsigned
\$14CA \$14CC \$14CE \$14D0 \$14D2	5322 5324 5326 5328 5330	2 2 2 2 2 2 2	MEMORY ALARM 6 MEMORY ALARM 7 MEMORY ALARM 8 MEMORY ALARM 9 MEMORY ALARM 10		W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned
\$14CA \$14CC \$14CE \$14D0 \$14D2 \$14D4	5322 5324 5326 5328 5330 5332	2 2 2 2 2 2 2 2 2	MEMORY ALARM 6 MEMORY ALARM 7 MEMORY ALARM 8 MEMORY ALARM 9 MEMORY ALARM 10 MEMORY ALARM 11		W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned
\$14CA \$14CC \$14CE \$14D0 \$14D2 \$14D4 \$14D6	5322 5324 5326 5328 5330 5332 5332 5334	2 2 2 2 2 2 2 2 2 2 2	MEMORY ALARM 6 MEMORY ALARM 7 MEMORY ALARM 8 MEMORY ALARM 9 MEMORY ALARM 10 MEMORY ALARM 11 MEMORY ALARM 12		W/R W/R W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned
\$14CA \$14CC \$14CE \$14D0 \$14D2 \$14D4 \$14D6 \$14D8	5322 5324 5326 5328 5330 5332 5332 5334 5336	2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY ALARM 6 MEMORY ALARM 7 MEMORY ALARM 8 MEMORY ALARM 9 MEMORY ALARM 10 MEMORY ALARM 11 MEMORY ALARM 12 MEMORY ALARM 13		W/R W/R W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$14CA \$14CC \$14CE \$14D0 \$14D2 \$14D4 \$14D6 \$14D8 \$14D8 \$14DA	5322 5324 5326 5328 5330 5332 5334 5336 5338	2 2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY ALARM 6 MEMORY ALARM 7 MEMORY ALARM 8 MEMORY ALARM 9 MEMORY ALARM 10 MEMORY ALARM 11 MEMORY ALARM 12 MEMORY ALARM 13 MEMORY ALARM 14		W/R W/R W/R W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$14CA \$14CC \$14CE \$14D0 \$14D2 \$14D4 \$14D6 \$14D8 \$14D8 \$14DA \$14DA \$14DC	5322 5324 5326 5328 5330 5332 5334 5336 5338 5338 5340	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY ALARM 6 MEMORY ALARM 7 MEMORY ALARM 8 MEMORY ALARM 9 MEMORY ALARM 10 MEMORY ALARM 10 MEMORY ALARM 11 MEMORY ALARM 12 MEMORY ALARM 13 MEMORY ALARM 14 MEMORY ALARM 15		W/R W/R W/R W/R W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$14CA \$14CC \$14CE \$14D0 \$14D2 \$14D4 \$14D6 \$14D8 \$14D8 \$14DA \$14DA \$14DC \$14DE	5322 5324 5326 5328 5330 5332 5334 5336 5338	2 2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY ALARM 6 MEMORY ALARM 7 MEMORY ALARM 8 MEMORY ALARM 9 MEMORY ALARM 10 MEMORY ALARM 11 MEMORY ALARM 12 MEMORY ALARM 13 MEMORY ALARM 14		W/R W/R W/R W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$14CA \$14CC \$14CE \$14D0 \$14D2 \$14D4 \$14D6 \$14D8 \$14D8 \$14DA \$14DA \$14DC	5322 5324 5326 5328 5330 5332 5334 5336 5338 5338 5340	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	MEMORY ALARM 6 MEMORY ALARM 7 MEMORY ALARM 8 MEMORY ALARM 9 MEMORY ALARM 10 MEMORY ALARM 10 MEMORY ALARM 11 MEMORY ALARM 12 MEMORY ALARM 13 MEMORY ALARM 14 MEMORY ALARM 15	0 = Min	W/R W/R W/R W/R W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned

¢14E2	5346	2	TYPE ALARM 2	1	W/R	Unciono
<u>\$14E2</u> \$14E4	5346 5348	2	TYPE ALARM 2 TYPE ALARM 3		W/R	Unsigne Unsigne
\$14E4 \$14E6	5350	2	TYPE ALARM 4		W/R	Unsigne
\$14E8	5352	2	TYPE ALARM 5		W/R	Unsigne
\$14E8	5354	2	TYPE ALARM 6		W/R	Unsigne
\$14EC	5356	2	TYPE ALARM 7		W/R	Unsigne
\$14EE	5358	2	TYPE ALARM 8		W/R	Unsigne
\$14F0	5360	2	TYPE ALARM 9		W/R	Unsigne
\$14F2	5362	2	TYPE ALARM 10		W/R	Unsigne
\$14F4	5364	2	TYPE ALARM 11		W/R	Unsigne
\$14F6	5366	2	TYPE ALARM 12		W/R	Unsigne
\$14F8	5368	2	TYPE ALARM 13		W/R	Unsigne
\$14FA	5370	2	TYPE ALARM 14		W/R	Unsigne
\$14FC	5372	2	TYPE ALARM 15		W/R	Unsigne
\$14FE	5374	2	TYPE ALARM 16		W/R	Unsigne
PPER THRESHO	5376	2	UPPER THRESHOLD ALARM 1	0000.0000 (def 0)	W/R	Signor
\$1500 \$1502	5376	2	UPPER THRESHOLD ALARM 1 UPPER THRESHOLD ALARM 2	-9999÷9999 (def.0)	W/R	Signed Signed
\$1502	5380	2	UPPER THRESHOLD ALARM 3		W/R	Signed
\$1506	5382		UPPER THRESHOLD ALARM 4		W/R	Signed
		2			W/R	0
\$1508	5384	2	UPPER THRESHOLD ALARM 5			Signed
\$150A	5386	2	UPPER THRESHOLD ALARM 6		W/R	Signed
\$150C	5388	2	UPPER THRESHOLD ALARM 7	-	W/R	Signed
\$150E	5390	2	UPPER THRESHOLD ALARM 8		W/R	Signed
\$1510	5392	2	UPPER THRESHOLD ALARM 9		W/R	Signed
\$1512	5394	2	UPPER THRESHOLD ALARM 10		W/R	Signe
\$1514	5396	2	UPPER THRESHOLD ALARM 11		W/R	Signe
\$1516	5398	2	UPPER THRESHOLD ALARM 12		W/R	Signe
\$1518	5400	2	UPPER THRESHOLD ALARM 13		W/R	Signe
\$151A	5402	2	UPPER THRESHOLD ALARM 14		W/R	Signe
\$151C	5404	2	UPPER THRESHOLD ALARM 15		W/R	Signe
\$151E	5406	2	UPPER THRESHOLD ALARM 16		W/R	Signe
PPER MULTIPLI	ER			0 = /100		
\$1520	5408	2	UPPER MULTIPLIER ALARM 1	1 = /10 2 = x1 (default) 3 = x10 4 = x100 5 = x1000	W/R	Unsigne
\$1522	5410	2	UPPER MULTIPLIER ALARM 2		W/R	Unsigne
\$1524	5412	2	UPPER MULTIPLIER ALARM 3		W/R	Unsigne
\$1526	5414	2	UPPER MULTIPLIER ALARM 4		W/R	Unsigne
\$1528	5416	2	UPPER MULTIPLIER ALARM 5		W/R	Unsigne
\$152A	5418	2	UPPER MULTIPLIER ALARM 6		W/R	Unsign
\$152C	5420	2	UPPER MULTIPLIER ALARM 7		W/R	Unsign
\$152E	5422	2	UPPER MULTIPLIER ALARM 8		W/R	Unsign
\$1530	5424	2	UPPER MULTIPLIER ALARM 9		W/R	Unsign
\$1532	5426	2	UPPER MULTIPLIER ALARM 10		W/R	Unsign
\$1534	5428	2	UPPER MULTIPLIER ALARM 11		W/R	Unsign
\$1536	5430	2	UPPER MULTIPLIER ALARM 12		W/R	Unsign
\$1538	5432	2	UPPER MULTIPLIER ALARM 13		W/R	Unsign
\$153A	5434	2	UPPER MULTIPLIER ALARM 14		W/R	Unsign
\$153C	5436	2	UPPER MULTIPLIER ALARM 15		W/R	Unsign
\$153E	5438	2	UPPER MULTIPLIER ALARM 16		W/R	Unsign
PPER DELAY	0400	Z			VV/IX	Unsign
\$1540	5440	2	UPPER DELAY ALARM 1	0 (default) ÷ 900s	W/R	Unsign
\$1542	5442	2	UPPER DELAY ALARM 2		W/R	Unsign
\$1544			UPPER DELAY ALARM 3		W/R	Unsign
	5444	2				Unsign
\$1546	5444 5446	2			W/R	
\$1546 \$1548	5446	2	UPPER DELAY ALARM 4		W/R W/R	Unsign
\$1548	5446 5448	2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5		W/R	0
\$1548 \$154A	5446 5448 5450	2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6		W/R W/R	Unsign
\$1548 \$154A \$154C	5446 5448 5450 5452	2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7		W/R W/R W/R	Unsign Unsign
\$1548 \$154A \$154C \$154E	5446 5448 5450 5452 5454	2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 8		W/R W/R W/R W/R	Unsign Unsign Unsign
\$1548 \$154A \$154C \$154E \$1550	5446 5448 5450 5452 5454 5454 5456	2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 8 UPPER DELAY ALARM 9		W/R W/R W/R W/R W/R	Unsign Unsign Unsign Unsign
\$1548 \$154A \$154C \$154E \$1550 \$1552	5446 5448 5450 5452 5454 5454 5456 5458	2 2 2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 8 UPPER DELAY ALARM 9 UPPER DELAY ALARM 10		W/R W/R W/R W/R W/R W/R	Unsign Unsign Unsign Unsign Unsign
\$1548 \$154A \$154C \$154E \$1550 \$1552 \$1554	5446 5448 5450 5452 5454 5456 5456 5458 5460	2 2 2 2 2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 7 UPPER DELAY ALARM 9 UPPER DELAY ALARM 10 UPPER DELAY ALARM 11		W/R W/R W/R W/R W/R W/R W/R	Unsign Unsign Unsign Unsign Unsign Unsign
\$1548 \$154A \$154C \$154E \$1550 \$1552 \$1554 \$1556	5446 5448 5450 5452 5454 5456 5456 5458 5460 5462	2 2 2 2 2 2 2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 7 UPPER DELAY ALARM 9 UPPER DELAY ALARM 10 UPPER DELAY ALARM 11 UPPER DELAY ALARM 12		W/R W/R W/R W/R W/R W/R W/R W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$1548 \$154A \$154C \$154E \$1550 \$1552 \$1554 \$1556 \$1558	5446 5448 5450 5452 5454 5456 5456 5458 5460 5462 5464	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 7 UPPER DELAY ALARM 9 UPPER DELAY ALARM 10 UPPER DELAY ALARM 11 UPPER DELAY ALARM 12 UPPER DELAY ALARM 13		W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$1548 \$154A \$154C \$154E \$1550 \$1552 \$1554 \$1556 \$1558 \$1558 \$155A	5446 5448 5450 5452 5454 5456 5458 5460 5462 5464 5466	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 7 UPPER DELAY ALARM 9 UPPER DELAY ALARM 10 UPPER DELAY ALARM 11 UPPER DELAY ALARM 12 UPPER DELAY ALARM 13 UPPER DELAY ALARM 14		W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$1548 \$154A \$154C \$154E \$1550 \$1552 \$1554 \$1556 \$1558 \$1558 \$155A \$155A	5446 5448 5450 5452 5454 5456 5458 5460 5462 5466 5468	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 7 UPPER DELAY ALARM 9 UPPER DELAY ALARM 10 UPPER DELAY ALARM 11 UPPER DELAY ALARM 12 UPPER DELAY ALARM 13 UPPER DELAY ALARM 14 UPPER DELAY ALARM 15		W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$1548 \$154A \$154C \$154E \$1550 \$1552 \$1554 \$1556 \$1558 \$1558 \$155A \$155A \$155C \$155E	5446 5448 5450 5452 5454 5456 5458 5460 5462 5466 5468 5470	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 7 UPPER DELAY ALARM 9 UPPER DELAY ALARM 10 UPPER DELAY ALARM 11 UPPER DELAY ALARM 12 UPPER DELAY ALARM 13 UPPER DELAY ALARM 14		W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$1548 \$154A \$154C \$1550 \$1552 \$1554 \$1556 \$1558 \$1558 \$1558 \$155A \$155C \$155E DWER THRESH	5446 5448 5450 5452 5454 5456 5458 5460 5462 5466 5468 5470	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 7 UPPER DELAY ALARM 9 UPPER DELAY ALARM 10 UPPER DELAY ALARM 10 UPPER DELAY ALARM 11 UPPER DELAY ALARM 12 UPPER DELAY ALARM 13 UPPER DELAY ALARM 14 UPPER DELAY ALARM 15 UPPER DELAY ALARM 16		W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign
\$1548 \$154A \$154C \$1550 \$1552 \$1554 \$1556 \$1558 \$1558 \$155A \$155C \$155E DWER THRESHO \$1560	5446 5448 5450 5452 5454 5456 5458 5460 5462 5466 5468 5470 5472	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 7 UPPER DELAY ALARM 9 UPPER DELAY ALARM 10 UPPER DELAY ALARM 10 UPPER DELAY ALARM 12 UPPER DELAY ALARM 13 UPPER DELAY ALARM 14 UPPER DELAY ALARM 15 UPPER DELAY ALARM 16 LOWER THRESHOLD ALARM 1	-9999÷9999 (def.0)	W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Signee
\$1548 \$154A \$154C \$154E \$1550 \$1552 \$1554 \$1556 \$1556 \$155A \$155C \$1555 \$1555 \$1555 \$1555 \$1555 \$1556 \$1556 \$1556 \$1556 \$1560 \$1560 \$1562	5446 5448 5450 5452 5454 5456 5458 5460 5462 5464 5466 5468 5466 5468 5470 OLD 5472 5474	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 7 UPPER DELAY ALARM 9 UPPER DELAY ALARM 9 UPPER DELAY ALARM 10 UPPER DELAY ALARM 11 UPPER DELAY ALARM 12 UPPER DELAY ALARM 13 UPPER DELAY ALARM 14 UPPER DELAY ALARM 15 UPPER DELAY ALARM 16 LOWER THRESHOLD ALARM 1 LOWER THRESHOLD ALARM 2	-9999÷9999 (def.0)	W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Signe
\$1548 \$154A \$154C \$154E \$1550 \$1552 \$1554 \$1556 \$1558 \$155A \$155C \$155E DWER THRESH \$1560 \$1562 \$1564	5446 5448 5450 5452 5454 5456 5458 5460 5462 5466 5468 5470 5472 5474 5474 5476	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 9 UPPER DELAY ALARM 9 UPPER DELAY ALARM 10 UPPER DELAY ALARM 11 UPPER DELAY ALARM 12 UPPER DELAY ALARM 13 UPPER DELAY ALARM 15 UPPER DELAY ALARM 16 LOWER THRESHOLD ALARM 1 LOWER THRESHOLD ALARM 2 LOWER THRESHOLD ALARM 3	-9999÷9999 (def.0)	W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Signe Signe
\$1548 \$154A \$154C \$1550 \$1552 \$1554 \$1556 \$1556 \$1555 \$1555 \$1555 \$1555 \$1555 \$1555 \$1555 \$1555 \$1555 \$1555 \$1556 \$1560 \$1562 \$1564 \$1566	5446 5448 5450 5452 5454 5456 5458 5460 5462 5466 5468 5470 5472 5474 5476 5478	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 8 UPPER DELAY ALARM 9 UPPER DELAY ALARM 10 UPPER DELAY ALARM 11 UPPER DELAY ALARM 12 UPPER DELAY ALARM 13 UPPER DELAY ALARM 14 UPPER DELAY ALARM 15 UPPER DELAY ALARM 16 LOWER THRESHOLD ALARM 1 LOWER THRESHOLD ALARM 2 LOWER THRESHOLD ALARM 3 LOWER THRESHOLD ALARM 4	-9999÷9999 (def.0)	W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Signe Signe Signe
\$1548 \$154A \$154C \$1550 \$1552 \$1554 \$1556 \$1558 \$1558 \$1555 OWER THRESH \$1560 \$1562 \$1564 \$1566 \$1568	5446 5448 5450 5452 5454 5456 5458 5460 5462 5466 5468 5468 5470 5472 5474 5478 5478 5480	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 8 UPPER DELAY ALARM 9 UPPER DELAY ALARM 10 UPPER DELAY ALARM 11 UPPER DELAY ALARM 12 UPPER DELAY ALARM 13 UPPER DELAY ALARM 13 UPPER DELAY ALARM 15 UPPER DELAY ALARM 16 UPPER THRESHOLD ALARM 1 LOWER THRESHOLD ALARM 2 LOWER THRESHOLD ALARM 3 LOWER THRESHOLD ALARM 4 LOWER THRESHOLD ALARM 4	-9999÷9999 (def.0)	W/R W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Signe Signe Signe
\$1548 \$154A \$154C \$1550 \$1552 \$1554 \$1556 \$1556 \$1555 \$1555 \$1555 \$1555 \$1555 \$1555 \$1555 \$1555 \$1555 \$1555 \$1556 \$1560 \$1562 \$1564 \$1566	5446 5448 5450 5452 5454 5456 5458 5460 5462 5466 5468 5470 5472 5474 5476 5478	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	UPPER DELAY ALARM 4 UPPER DELAY ALARM 5 UPPER DELAY ALARM 6 UPPER DELAY ALARM 6 UPPER DELAY ALARM 7 UPPER DELAY ALARM 8 UPPER DELAY ALARM 9 UPPER DELAY ALARM 10 UPPER DELAY ALARM 11 UPPER DELAY ALARM 12 UPPER DELAY ALARM 13 UPPER DELAY ALARM 14 UPPER DELAY ALARM 15 UPPER DELAY ALARM 16 LOWER THRESHOLD ALARM 1 LOWER THRESHOLD ALARM 2 LOWER THRESHOLD ALARM 3 LOWER THRESHOLD ALARM 4	-9999÷9999 (def.0)	W/R	Unsign Unsign Unsign Unsign Unsign Unsign Unsign Unsign Signe Signe Signe

\$156E	5486	2	LOWER THRESHOLD ALARM 8		W/R	Signec
\$1570	5488	2	LOWER THRESHOLD ALARM 9		W/R	Signec
\$1572	5490	2	LOWER THRESHOLD ALARM 10		W/R	Signec
\$1574	5492	2	LOWER THRESHOLD ALARM 11		W/R	Signed
\$1576	5494	2	LOWER THRESHOLD ALARM 12		W/R	Signed
\$1578	5496	2	LOWER THRESHOLD ALARM 13		W/R	Signed
\$157A	5498	2	LOWER THRESHOLD ALARM 14		W/R	Signed
\$157C	5500	2	LOWER THRESHOLD ALARM 15		W/R	Signed
\$157E	5502	2	LOWER THRESHOLD ALARM 16		W/R	Signed
		-				eignee
\$1580	5504	2	LOWER MULTIPLIER ALARM 1	0 = /100 1 = /10 2 = x1 (default) 3 = x10 4 = x100 5 = x1000	W/R	Unsigne
\$1582	5506	2	LOWER MULTIPLIER ALARM 2		W/R	Unsigne
\$1584	5508	2	LOWER MULTIPLIER ALARM 3		W/R	Unsigne
\$1586	5510	2	LOWER MULTIPLIER ALARM 4		W/R	Unsigne
\$1588	5512	2	LOWER MULTIPLIER ALARM 5		W/R	Unsigne
\$158A	5514	2	LOWER MULTIPLIER ALARM 6		W/R	Unsigne
\$158C	5516	2	LOWER MULTIPLIER ALARM 7		W/R	Unsigne
\$158E	5518	2	LOWER MULTIPLIER ALARM 8		W/R	Unsigne
\$158E \$1590	5520	2	LOWER MULTIPLIER ALARM 8		W/R	0
						Unsigne
\$1592	5522	2	LOWER MULTIPLIER ALARM 10		W/R	Unsigne
\$1594	5524	2	LOWER MULTIPLIER ALARM 11		W/R	Unsigne
\$1596	5526	2	LOWER MULTIPLIER ALARM 12		W/R	Unsigne
\$1598	5528	2	LOWER MULTIPLIER ALARM 13		W/R	Unsigne
\$159A	5530	2	LOWER MULTIPLIER ALARM 14		W/R	Unsigne
\$159C	5532	2	LOWER MULTIPLIER ALARM 15		W/R	Unsigne
\$159E	5534	2	LOWER MULTIPLIER ALARM 16		W/R	Unsigne
OWER DELAY						
\$15A0	5536	2	LOWER DELAY ALARM 1	0 (default) ÷ 900s	W/R	Unsigne
\$15A2	5538	2	LOWER DELAY ALARM 2		W/R	Unsigne
\$15A4	5540	2	LOWER DELAY ALARM 3		W/R	Unsigne
\$15A6	5542	2	LOWER DELAY ALARM 4		W/R	Unsigne
\$15A8	5544	2	LOWER DELAY ALARM 5		W/R	Unsigne
\$15AA	5546	2	LOWER DELAY ALARM 6		W/R	Unsigne
						0
\$15AC	5548	2	LOWER DELAY ALARM 7		W/R	Unsigne
\$15AE	5550	2	LOWER DELAY ALARM 8		W/R	Unsigne
\$15B0	5552	2	LOWER DELAY ALARM 9		W/R	Unsigne
\$15B2	5554	2	LOWER DELAY ALARM 10		W/R	Unsigne
\$15B4	5556	2	LOWER DELAY ALARM 11		W/R	Unsigne
\$15B6	5558	2	LOWER DELAY ALARM 12		W/R	Unsigne
\$15B8	5560	2	LOWER DELAY ALARM 13		W/R	Unsigne
\$15BA	5562	2	LOWER DELAY ALARM 14		W/R	Unsigne
\$15BC	5564	2	LOWER DELAY ALARM 15		W/R	Unsigne
\$15BE	5566	2	LOWER DELAY ALARM 16		W/R	Unsigne
EST STATUS	0000	-				onoigh
\$15C0	5568	2	REST STATUS ALARM 1	0 = OFF (default) 1 = ON	W/R	Unsigne
\$15C2	5570	2	REST STATUS ALARM 2		W/R	Unsigne
\$15C4	5572	2	REST STATUS ALARM 3		W/R	Unsigne
\$15C6	5574	2	REST STATUS ALARM 3		W/R	Unsigne
\$15C8	5576	2	REST STATUS ALARM 5		W/R	Unsigne
\$15CA	5578	2	REST STATUS ALARM 6		W/R	Unsigne
\$15CC	5580	2	REST STATUS ALARM 7		W/R	Unsigne
\$15CE	5582	2	REST STATUS ALARM 8		W/R	Unsigne
\$15D0	5584	2	REST STATUS ALARM 9		W/R	Unsigne
\$15D2	5586	2	REST STATUS ALARM 10		W/R	Unsigne
\$15D4	5588	2	REST STATUS ALARM 11		W/R	Unsigne
\$15D6	5590	2	REST STATUS ALARM 12		W/R	Unsigne
\$15D8	5592	2	REST STATUS ALARM 13		W/R	Unsigne
\$15DA	5594	2	REST STATUS ALARM 14		W/R	Unsigne
\$15DC	5596	2	REST STATUS ALARM 15		W/R	Unsigne
\$15DE	5598	2	REST STATUS ALARM 16		W/R	Unsigne
GITAL OUTPUT						2
				0 = OFF (default)		
\$15E0	5600	2	DIG.OUT FOR ALARM 1	1 = DO1 2 = DO2 3 = DO1+DO2	W/R	Unsigne
\$15E2	5602	2	DIG.OUT FOR ALARM 2		W/R	Unsigne
\$15E4	5604	2	DIG.OUT FOR ALARM 2 DIG.OUT FOR ALARM 3		W/R	Unsign
\$15E6	5606	2	DIG.OUT FOR ALARM 5 DIG.OUT FOR ALARM 4		W/R	Unsigne
					W/R	0
\$15E8	5608	2	DIG.OUT FOR ALARM 5			Unsigne
\$15EA	5610	2	DIG.OUT FOR ALARM 6		W/R	Unsigne
\$15EC	5612	2	DIG.OUT FOR ALARM 7		W/R	Unsigne
	5614	1 2	DIG.OUT FOR ALARM 8		W/R	Unsigne
\$15EE \$15F0	5616	2	DIG.OUT FOR ALARM 9		W/R	Unsigne

\$15F2	5618	2	DIG.OUT FOR ALARM 10		W/R	Unsigne
\$15F4	5620	2	DIG.OUT FOR ALARM 11		W/R	Unsigne
\$15F6	5622	2	DIG.OUT FOR ALARM 12		W/R	Unsigne
\$15F8	5624	2	DIG.OUT FOR ALARM 13		W/R	Unsigne
\$15FA	5626	2	DIG.OUT FOR ALARM 14		W/R	Unsigne
\$15FC	5628	2	DIG.OUT FOR ALARM 15		W/R	Unsigne
\$15FE	5630	2	DIG.OUT FOR ALARM 16		W/R	Unsigne
				·	•	
			INPUTS			
JNCTION						
\$1600	5632	2	FUNCTION INPUT 1	0 = OFF (default) 1 = ON 2 = SYNC. 3 = TAR.1 4 = TAR.2	W/R	Unsigne
\$1602	5634	2	FUNCTION INPUT 2		W/R	Unsigne
\$1604	5636	2	FUNCTION INPUT 3		W/R	Unsigne
\$1606	5638	2	FUNCTION INPUT 4		W/R	Unsigne
\$1608	5640	2	FUNCTION INPUT 5		W/R	Unsigne
\$160A	5642	2	FUNCTION INPUT 6		W/R	Unsigne
\$160C	5644	2	FUNCTION INPUT 7		W/R	Unsigne
\$160E	5646	2	FUNCTION INPUT 8		W/R	Unsigne
\$1610	5648	2	FUNCTION INPUT 9		W/R	Unsigne
\$1612	5650	2	FUNCTION INPUT 10		W/R	Unsigne
\$1614	5652	2	FUNCTION INPUT 11		W/R	Unsign
\$1616	5654	2	FUNCTION INPUT 12		W/R	Unsign
\$1618	5656	2	FUNCTION INPUT 13		W/R	Unsigne
\$1618 \$161A	5658	2	FUNCTION INPUT 13		W/R	Unsign
	<u> </u>		FUNCTION INPUT 14 FUNCTION INPUT 15		W/R W/R	5
\$161C		2				Unsign
\$161E EST STATUS	5662	2	FUNCTION INPUT 16		W/R	Unsign
\$1620	5664	2	REST STATUS INPUT 1	0 = OFF (default)	W/R	Unsign
, 				1 = ON		•
\$1622	5666	2	REST STATUS INPUT 2		W/R	Unsign
\$1624	5668	2	REST STATUS INPUT 3		W/R	Unsign
\$1626	5670	2	REST STATUS INPUT 4		W/R	Unsign
\$1628	5672	2	REST STATUS INPUT 5		W/R	Unsign
\$162A	5674	2	REST STATUS INPUT 6		W/R	Unsign
\$162C	5676	2	REST STATUS INPUT 7		W/R	Unsign
\$162E	5678	2	REST STATUS INPUT 8		W/R	Unsigne
\$1630	5680	2	REST STATUS INPUT 9		W/R	Unsign
\$1632	5682	2	REST STATUS INPUT 10		W/R	Unsigne
\$1634	5684	2	REST STATUS INPUT 11		W/R	Unsigne
\$1636	5686	2	REST STATUS INPUT 12		W/R	Unsign
\$1638	5688	2	REST STATUS INPUT 13		W/R	Unsigne
\$163A	5690	2	REST STATUS INPUT 14		W/R	Unsign
\$163C	5692	2	REST STATUS INPUT 15		W/R	Unsign
\$163E	5694	2	REST STATUS INPUT 16		W/R	Unsign
PERAND 1			BOOLEAN			
\$1700	5888	2	OPERAND 1 BOOLEAN 1	0 = OFF (default) 1÷16 = Input 1÷16 17÷32 = Alarm. 1÷16 33÷48 = Bool 1÷16	W/R	Unsign
\$1702	5890	2	OPERAND 1 BOOLEAN 2		W/R	Unsigne
\$1704	5892	2	OPERAND 1 BOOLEAN 3		W/R	Unsign
\$1706	5894	2	OPERAND 1 BOOLEAN 4		W/R	Unsign
\$1708	5896	2	OPERAND 1 BOOLEAN 5		W/R	Unsign
\$170A	5898	2	OPERAND 1 BOOLEAN 6		W/R	Unsign
\$170C	5900	2	OPERAND 1 BOOLEAN 7		W/R	Unsign
\$170E	5902	2	OPERAND 1 BOOLEAN 8		W/R	Unsign
ψΠOL	5904	2	OPERAND 1 BOOLEAN 9		W/R	Unsign
\$1710			OPERAND 1 BOOLEAN 10		W/R	Unsign
	5906	2				
\$1710 \$1712	5906		OPERAND 1 BOOLEAN 11		W/R	Unsign
\$1710 \$1712 \$1714	5906 5908	2	OPERAND 1 BOOLEAN 11 OPERAND 1 BOOLEAN 12		W/R W/R	
\$1710 \$1712 \$1714 \$1716	5906 5908 5910	2	OPERAND 1 BOOLEAN 12		W/R	Unsign
\$1710 \$1712 \$1714 \$1716 \$1718	5906 5908 5910 5912	2 2 2	OPERAND 1 BOOLEAN 12 OPERAND 1 BOOLEAN 13		W/R W/R	Unsign Unsign
\$1710 \$1712 \$1714 \$1716 \$1718 \$1718 \$171A	5906 5908 5910 5912 5914	2 2 2 2 2	OPERAND 1 BOOLEAN 12 OPERAND 1 BOOLEAN 13 OPERAND 1 BOOLEAN 14		W/R W/R W/R	Unsign Unsign Unsign
\$1710 \$1712 \$1714 \$1716 \$1718	5906 5908 5910 5912	2 2 2	OPERAND 1 BOOLEAN 12 OPERAND 1 BOOLEAN 13		W/R W/R	Unsign Unsign Unsign Unsign
\$1710 \$1712 \$1714 \$1716 \$1718 \$1718 \$171A \$171C	5906 5908 5910 5912 5914 5916	2 2 2 2 2 2 2	OPERAND 1 BOOLEAN 12 OPERAND 1 BOOLEAN 13 OPERAND 1 BOOLEAN 14 OPERAND 1 BOOLEAN 15		W/R W/R W/R W/R	Unsign Unsign Unsign Unsign
\$1710 \$1712 \$1714 \$1716 \$1718 \$1718 \$171A \$171C \$171E	5906 5908 5910 5912 5914 5916	2 2 2 2 2 2 2	OPERAND 1 BOOLEAN 12 OPERAND 1 BOOLEAN 13 OPERAND 1 BOOLEAN 14 OPERAND 1 BOOLEAN 15	0 = (default) 1 = AND 2 = OR	W/R W/R W/R W/R	Unsign Unsign Unsign Unsign
\$1710 \$1712 \$1714 \$1716 \$1718 \$1718 \$171A \$171C \$171E PERATOR \$1720	5906 5908 5910 5912 5914 5916 5918 5920	2 2 2 2 2 2 2 2 2 2 2	OPERAND 1 BOOLEAN 12 OPERAND 1 BOOLEAN 13 OPERAND 1 BOOLEAN 14 OPERAND 1 BOOLEAN 15 OPERAND 1 BOOLEAN 16 OPERATOR BOOLEAN 1		W/R W/R W/R W/R W/R	Unsign Unsign Unsign Unsign Unsign
\$1710 \$1712 \$1714 \$1716 \$1718 \$1718 \$171A \$171C \$171E PERATOR \$1720 \$1722	5906 5908 5910 5912 5914 5916 5918 5920 5922	2 2 2 2 2 2 2 2 2 2 2 2 2 2	OPERAND 1 BOOLEAN 12 OPERAND 1 BOOLEAN 13 OPERAND 1 BOOLEAN 14 OPERAND 1 BOOLEAN 15 OPERAND 1 BOOLEAN 16 OPERATOR BOOLEAN 1 OPERATOR BOOLEAN 2	1 = AND	W/R W/R W/R W/R W/R W/R	Unsign Unsign Unsign Unsign Unsign
\$1710 \$1712 \$1714 \$1716 \$1718 \$1718 \$171A \$171C \$171C \$171E PERATOR \$1720 \$1722 \$1724	5906 5908 5910 5912 5914 5916 5918 5920 5922 5924	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	OPERAND 1 BOOLEAN 12 OPERAND 1 BOOLEAN 13 OPERAND 1 BOOLEAN 14 OPERAND 1 BOOLEAN 15 OPERAND 1 BOOLEAN 16 OPERATOR BOOLEAN 1 OPERATOR BOOLEAN 2 OPERATOR BOOLEAN 3	1 = AND	W/R W/R W/R W/R W/R W/R	Unsign Unsign Unsign Unsign Unsign
\$1710 \$1712 \$1714 \$1716 \$1718 \$1718 \$171A \$171C \$171E PERATOR \$1720 \$1722	5906 5908 5910 5912 5914 5916 5918 5920 5922	2 2 2 2 2 2 2 2 2 2 2 2 2 2	OPERAND 1 BOOLEAN 12 OPERAND 1 BOOLEAN 13 OPERAND 1 BOOLEAN 14 OPERAND 1 BOOLEAN 15 OPERAND 1 BOOLEAN 16 OPERATOR BOOLEAN 1 OPERATOR BOOLEAN 2	1 = AND	W/R W/R W/R W/R W/R W/R	Unsigne Unsigne Unsigne Unsigne Unsigne Unsigne Unsigne Unsigne Unsigne

\$172C	5932	2	OPERATOR BOOLEAN 7	1	W/R	Unsigned
\$172C \$172E	5934	2	OPERATOR BOOLEAN 7		W/R	Unsigned
\$1730	5936	2	OPERATOR BOOLEAN 9		W/R	Unsigned
\$1732	5938	2	OPERATOR BOOLEAN 10		W/R	Unsigned
\$1734	5940	2	OPERATOR BOOLEAN 11		W/R	Unsigned
\$1736	5942	2	OPERATOR BOOLEAN 12		W/R	Unsigned
\$1738	5944	2	OPERATOR BOOLEAN 13		W/R	Unsigned
\$173A	5946	2	OPERATOR BOOLEAN 14		W/R	Unsigned
\$173C	5948	2	OPERATOR BOOLEAN 15		W/R	Unsigned
\$173E	5950	2	OPERATOR BOOLEAN 16		W/R	Unsigned
OPERAND 2						
				0 = OFF (default)		
\$1740	5952	2	OPERAND 2 BOOLEAN 1	$1\div16 = \text{Input } 1\div16$	W/R	Unsigned
				$17 \div 32 = Alarm. 1 \div 16$		U
\$1742	E0E4	2	OPERAND 2 BOOLEAN 2	33÷48 = Bool 1÷16	W/R	Unsigned
\$1744	<u>5954</u> 5956	2	OPERAND 2 BOOLEAN 2 OPERAND 2 BOOLEAN 3		W/R	Unsigned Unsigned
\$1746	5958	2	OPERAND 2 BOOLEAN 4		W/R	Unsigned
\$1748	5960	2	OPERAND 2 BOOLEAN 5		W/R	Unsigned
\$174A	5962	2	OPERAND 2 BOOLEAN 6		W/R	Unsigned
\$174C	5964	2	OPERAND 2 BOOLEAN 7		W/R	Unsigned
\$174E	5966	2	OPERAND 2 BOOLEAN 8		W/R	Unsigned
\$1750	5968	2	OPERAND 2 BOOLEAN 9		W/R	Unsigned
\$1752	5970	2	OPERAND 2 BOOLEAN 10		W/R	Unsigned
\$1754	5972	2	OPERAND 2 BOOLEAN 11		W/R	Unsigned
\$1756	5974	2	OPERAND 2 BOOLEAN 12		W/R	Unsigned
\$1758	5976	2	OPERAND 2 BOOLEAN 13		W/R	Unsigned
\$175A	5978	2	OPERAND 2 BOOLEAN 14		W/R	Unsigned
\$175C	5980	2	OPERAND 2 BOOLEAN 15		W/R	Unsigned
\$175E	5982	2	OPERAND 2 BOOLEAN 16		W/R	Unsigned
* · · · * =		—	MATHEMATICS			energine e
VISUALIZATION						
\$1800	6144	2	VISUALIZATION MATHEMATHICS 1	0 = OFF (default)	W/R	Unsigned
	0144	2		1 = ON		Unsigned
\$1802	6146	2	VISUALIZATION MATHEMATHICS 2		W/R	Unsigned
\$1804	6148	2	VISUALIZATION MATHEMATHICS 3		W/R	Unsigned
\$1806	6150	2	VISUALIZATION MATHEMATHICS 4		W/R	Unsigned
\$1808	6152	2	VISUALIZATION MATHEMATHICS 5		W/R	Unsigned
\$180A	6154	2	VISUALIZATION MATHEMATHICS 6		W/R	Unsigned
\$180C	6156	2	VISUALIZATION MATHEMATHICS 7		W/R	Unsigned
\$180E	6158	2	VISUALIZATION MATHEMATHICS 8		W/R	Unsigned
\$1810	6160	2	VISUALIZATION MATHEMATHICS 9		W/R	Unsigned
\$1812	6162	2	VISUALIZATION MATHEMATHICS 10		W/R	Unsigned
\$1814	6164	2	VISUALIZATION MATHEMATHICS 11		W/R	Unsigned
\$1816	6166	2	VISUALIZATION MATHEMATHICS 12		W/R	Unsigned
\$1818	6168	2	VISUALIZATION MATHEMATHICS 13		W/R	Unsigned
\$181A	6170	2	VISUALIZATION MATHEMATHICS 14		W/R	Unsigned
\$181C	6172	2	VISUALIZATION MATHEMATHICS 15		W/R	Unsigned
\$181E	6174	2	VISUALIZATION MATHEMATHICS 16		W/R	Unsigned
DESCRIPTION						
¢4000	0470	10		Up to 20 characters	W/R	L lucations and
\$1820	6176	10	DESCRIPTION MATHEMATHICS 1	(\$20÷\$7A) (def.: Math 01)	VV/K	Unsigned
\$182A	6186	10	DESCRIPTION MATHEMATHICS 2	(def.: Math 02)	W/R	Unsigned
\$1834	6196		DESCRIPTION MATHEMATHICS 2 DESCRIPTION MATHEMATHICS 3	· · · · · · · · · · · · · · · · · · ·		
+				(dot · Moth O')		
\$192E		10		(def.: Math 03)	W/R	J
\$183E	6206	10	DESCRIPTION MATHEMATHICS 4	(def.: Math 04)	W/R W/R	Unsigned
\$1848	6206 6216	10 10	DESCRIPTION MATHEMATHICS 4 DESCRIPTION MATHEMATHICS 5	(def.: Math 04) (def.: Math 05)	W/R W/R W/R	Unsigned Unsigned
\$1848 \$1852	6206 6216 6226	10 10 10	DESCRIPTION MATHEMATHICS 4 DESCRIPTION MATHEMATHICS 5 DESCRIPTION MATHEMATHICS 6	(def.: Math 04) (def.: Math 05) (def.: Math 06)	W/R W/R W/R W/R	Unsigned Unsigned Unsigned
\$1848 \$1852 \$185C	6206 6216 6226 6236	10 10 10 10	DESCRIPTION MATHEMATHICS 4 DESCRIPTION MATHEMATHICS 5 DESCRIPTION MATHEMATHICS 6 DESCRIPTION MATHEMATHICS 7	(def.: Math 04) (def.: Math 05) (def.: Math 06) (def.: Math 07)	W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned
\$1848 \$1852 \$185C \$1866	6206 6216 6226 6236 6236 6246	10 10 10 10 10	DESCRIPTION MATHEMATHICS 4 DESCRIPTION MATHEMATHICS 5 DESCRIPTION MATHEMATHICS 6 DESCRIPTION MATHEMATHICS 7 DESCRIPTION MATHEMATHICS 8	(def.: Math 04) (def.: Math 05) (def.: Math 06) (def.: Math 07) (def.: Math 07)	W/R W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned
\$1848 \$1852 \$185C \$1866 \$1870	6206 6216 6226 6236 6246 6256	10 10 10 10 10 10 10	DESCRIPTION MATHEMATHICS 4 DESCRIPTION MATHEMATHICS 5 DESCRIPTION MATHEMATHICS 6 DESCRIPTION MATHEMATHICS 7 DESCRIPTION MATHEMATHICS 8 DESCRIPTION MATHEMATHICS 9	(def.: Math 04) (def.: Math 05) (def.: Math 06) (def.: Math 07) (def.: Math 07) (def.: Math 08) (def.: Math 09)	W/R W/R W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$1848 \$1852 \$185C \$1866 \$1870 \$187A	6206 6216 6226 6236 6246 6256 6266	10 10 10 10 10 10 10 10	DESCRIPTION MATHEMATHICS 4 DESCRIPTION MATHEMATHICS 5 DESCRIPTION MATHEMATHICS 6 DESCRIPTION MATHEMATHICS 7 DESCRIPTION MATHEMATHICS 8 DESCRIPTION MATHEMATHICS 9 DESCRIPTION MATHEMATHICS 10	(def.: Math 04) (def.: Math 05) (def.: Math 06) (def.: Math 07) (def.: Math 07) (def.: Math 08) (def.: Math 09) (def.: Math 10)	W/R W/R W/R W/R W/R W/R W/R W/R W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$1848 \$1852 \$185C \$1866 \$1870 \$187A \$1884	6206 6216 6226 6236 6246 6256 6266 6266 6276	10 10 10 10 10 10 10 10 10	DESCRIPTION MATHEMATHICS 4 DESCRIPTION MATHEMATHICS 5 DESCRIPTION MATHEMATHICS 6 DESCRIPTION MATHEMATHICS 7 DESCRIPTION MATHEMATHICS 8 DESCRIPTION MATHEMATHICS 9 DESCRIPTION MATHEMATHICS 10 DESCRIPTION MATHEMATHICS 11	(def.: Math 04) (def.: Math 05) (def.: Math 06) (def.: Math 07) (def.: Math 07) (def.: Math 08) (def.: Math 09) (def.: Math 10) (def.: Math 11)	W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$1848 \$1852 \$185C \$1866 \$1870 \$187A \$1884 \$188E	6206 6216 6226 6236 6246 6256 6266 6276 6286	10 10 10 10 10 10 10 10 10 10	DESCRIPTION MATHEMATHICS 4 DESCRIPTION MATHEMATHICS 5 DESCRIPTION MATHEMATHICS 6 DESCRIPTION MATHEMATHICS 7 DESCRIPTION MATHEMATHICS 8 DESCRIPTION MATHEMATHICS 9 DESCRIPTION MATHEMATHICS 10 DESCRIPTION MATHEMATHICS 11 DESCRIPTION MATHEMATHICS 12	(def.: Math 04) (def.: Math 05) (def.: Math 06) (def.: Math 07) (def.: Math 07) (def.: Math 08) (def.: Math 09) (def.: Math 10) (def.: Math 11) (def.: Math 12)	W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$1848 \$1852 \$185C \$1866 \$1870 \$187A \$1884 \$188E \$1898	6206 6216 6226 6236 6246 6256 6266 6276 6286 6296	10 10 10 10 10 10 10 10 10 10	DESCRIPTION MATHEMATHICS 4 DESCRIPTION MATHEMATHICS 5 DESCRIPTION MATHEMATHICS 6 DESCRIPTION MATHEMATHICS 7 DESCRIPTION MATHEMATHICS 8 DESCRIPTION MATHEMATHICS 9 DESCRIPTION MATHEMATHICS 10 DESCRIPTION MATHEMATHICS 11 DESCRIPTION MATHEMATHICS 12 DESCRIPTION MATHEMATHICS 13	(def.: Math 04) (def.: Math 05) (def.: Math 06) (def.: Math 07) (def.: Math 07) (def.: Math 08) (def.: Math 09) (def.: Math 10) (def.: Math 11) (def.: Math 12) (def.: Math 13)	W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$1848 \$1852 \$185C \$1866 \$1870 \$187A \$1884 \$188E \$1888 \$1898 \$18A2	6206 6216 6226 6236 6246 6256 6266 6276 6286 6296 6306	10 10 10 10 10 10 10 10 10 10 10	DESCRIPTION MATHEMATHICS 4 DESCRIPTION MATHEMATHICS 5 DESCRIPTION MATHEMATHICS 6 DESCRIPTION MATHEMATHICS 7 DESCRIPTION MATHEMATHICS 8 DESCRIPTION MATHEMATHICS 9 DESCRIPTION MATHEMATHICS 10 DESCRIPTION MATHEMATHICS 11 DESCRIPTION MATHEMATHICS 12 DESCRIPTION MATHEMATHICS 13 DESCRIPTION MATHEMATHICS 14	(def.: Math 04) (def.: Math 05) (def.: Math 06) (def.: Math 07) (def.: Math 07) (def.: Math 09) (def.: Math 10) (def.: Math 11) (def.: Math 12) (def.: Math 13) (def.: Math 14)	W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
\$1848 \$1852 \$185C \$1866 \$1870 \$187A \$1884 \$188E \$188E \$1898 \$18A2 \$18A2 \$18AC	6206 6216 6226 6236 6246 6256 6266 6276 6286 6296 6306 6316	10 10 10 10 10 10 10 10 10 10 10 10	DESCRIPTION MATHEMATHICS 4 DESCRIPTION MATHEMATHICS 5 DESCRIPTION MATHEMATHICS 6 DESCRIPTION MATHEMATHICS 7 DESCRIPTION MATHEMATHICS 8 DESCRIPTION MATHEMATHICS 9 DESCRIPTION MATHEMATHICS 10 DESCRIPTION MATHEMATHICS 11 DESCRIPTION MATHEMATHICS 12 DESCRIPTION MATHEMATHICS 13 DESCRIPTION MATHEMATHICS 14 DESCRIPTION MATHEMATHICS 14	(def.: Math 04) (def.: Math 05) (def.: Math 06) (def.: Math 07) (def.: Math 07) (def.: Math 09) (def.: Math 10) (def.: Math 11) (def.: Math 12) (def.: Math 12) (def.: Math 13) (def.: Math 14) (def.: Math 15)	W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
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\$1848 \$1852 \$185C \$1866 \$1870 \$187A \$1884 \$188E \$1898 \$18A2 \$18A2 \$18AC \$18B6 MEASURE UNIT	6206 6216 6226 6236 6246 6256 6266 6276 6286 6296 6306 6316 6326	10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10 10	DESCRIPTION MATHEMATHICS 4 DESCRIPTION MATHEMATHICS 5 DESCRIPTION MATHEMATHICS 6 DESCRIPTION MATHEMATHICS 7 DESCRIPTION MATHEMATHICS 8 DESCRIPTION MATHEMATHICS 9 DESCRIPTION MATHEMATHICS 10 DESCRIPTION MATHEMATHICS 11 DESCRIPTION MATHEMATHICS 12 DESCRIPTION MATHEMATHICS 13 DESCRIPTION MATHEMATHICS 14 DESCRIPTION MATHEMATHICS 15 DESCRIPTION MATHEMATHICS 16	(def.: Math 04) (def.: Math 05) (def.: Math 06) (def.: Math 07) (def.: Math 08) (def.: Math 09) (def.: Math 10) (def.: Math 11) (def.: Math 12) (def.: Math 13) (def.: Math 14) (def.: Math 15) (def.: Math 16)	W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
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\$1848 \$1852 \$185C \$1866 \$1870 \$187A \$1884 \$1884 \$1888 \$1898 \$18A2 \$18AC \$18B6 MEASURE UNIT \$18C0	6206 6216 6226 6236 6246 6256 6266 6276 6286 6306 6316 6336	10 10 10 10 10 10 10 10 10 10 10 10 3	DESCRIPTION MATHEMATHICS 4 DESCRIPTION MATHEMATHICS 5 DESCRIPTION MATHEMATHICS 6 DESCRIPTION MATHEMATHICS 7 DESCRIPTION MATHEMATHICS 8 DESCRIPTION MATHEMATHICS 9 DESCRIPTION MATHEMATHICS 10 DESCRIPTION MATHEMATHICS 11 DESCRIPTION MATHEMATHICS 12 DESCRIPTION MATHEMATHICS 13 DESCRIPTION MATHEMATHICS 14 DESCRIPTION MATHEMATHICS 15 DESCRIPTION MATHEMATHICS 15 DESCRIPTION MATHEMATHICS 16 M.U. MATHEMATHICS 1	(def.: Math 04) (def.: Math 05) (def.: Math 06) (def.: Math 07) (def.: Math 08) (def.: Math 09) (def.: Math 10) (def.: Math 11) (def.: Math 12) (def.: Math 13) (def.: Math 14) (def.: Math 15) (def.: Math 16)	W/R	Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned Unsigned
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\$18D8	6360	3	M.U. MATHEMATHICS 9	W/R	Unsigne
\$18DB	6363	3	M.U. MATHEMATHICS 10	W/R	Unsigne
\$18DE	6366	3	M.U. MATHEMATHICS 11	W/R	Unsigne
\$18E1	6369	3	M.U. MATHEMATHICS 12	W/R	Unsigne
\$18E4	6372	3	M.U. MATHEMATHICS 13	W/R	Unsign
\$18E7	6375	3	M.U. MATHEMATHICS 14	W/R	Unsign
\$18EA	6378	3	M.U. MATHEMATHICS 15	W/R	Unsign
\$18ED	6381	3	M.U. MATHEMATHICS 16	W/R	Unsign
PERAND 1					Ŭ
\$18F0	6384	2	OPERAND 1 MATHEMATHICS 1 $0 = OFF$ (default) $1\div16 = Cnt.Tot 1\div1$ $17\div32 = Cnt.Par. 1$ $33\div48 = Derivate 1$ $49\div64 = Math. 1\div1$	÷16 W/R ÷16	Unsigne
\$18F2	6386	2	OPERAND 1 MATHEMATHICS 2	W/R	Unsign
\$18F4	6388	2	OPERAND 1 MATHEMATHICS 3	W/R	Unsign
\$18F6	6390	2	OPERAND 1 MATHEMATHICS 4	W/R	Unsign
\$18F8	6392	2	OPERAND 1 MATHEMATHICS 5	W/R	Unsign
\$18FA	6394	2	OPERAND 1 MATHEMATHICS 6	W/R	Unsign
\$18FC	6396	2	OPERAND 1 MATHEMATHICS 7	W/R	Unsign
\$18FE	6398	2	OPERAND 1 MATHEMATHICS 8	W/R	Unsign
\$1900	6400	2	OPERAND 1 MATHEMATHICS 8 OPERAND 1 MATHEMATHICS 9	W/R	Unsign
\$1900	6400	2	OPERAND 1 MATHEMATHICS 9 OPERAND 1 MATHEMATHICS 10	W/R	Unsign
\$1902					
	6404	2	OPERAND 1 MATHEMATHICS 11	W/R	Unsign
\$1906	6406	2	OPERAND 1 MATHEMATHICS 12	W/R	Unsign
\$1908	6408	2	OPERAND 1 MATHEMATHICS 13	W/R	Unsign
\$190A	6410	2	OPERAND 1 MATHEMATHICS 14	W/R	Unsign
\$190C	6412	2	OPERAND 1 MATHEMATHICS 15	W/R	Unsign
\$190E	6414	2	OPERAND 1 MATHEMATHICS 16	W/R	Unsign
PERATOR \$1910	6416	2	OPERATOR MATHEMATHICS 1 0 = + (default) 1 = - 2 = /	W/R	Unsign
\$1912	6418	2	OPERATOR MATHEMATHICS 2	W/R	Unsign
\$1914	6420	2	OPERATOR MATHEMATHICS 3	W/R	Unsign
\$1916	6422	2	OPERATOR MATHEMATHICS 4	W/R	Unsign
\$1918	6424	2	OPERATOR MATHEMATHICS 5	W/R	Unsign
\$191A	6426	2	OPERATOR MATHEMATHICS 6	W/R	Unsign
\$191C	6428	2	OPERATOR MATHEMATHICS 7	W/R	Unsign
\$191E	6430	2	OPERATOR MATHEMATHICS 7	W/R	
			OPERATOR MATHEMATHICS 8 OPERATOR MATHEMATHICS 9		Unsign
\$1920	6432	2		W/R	Unsign
\$1922	6434	2	OPERATOR MATHEMATHICS 10	W/R	Unsign
\$1924	6436	2	OPERATOR MATHEMATHICS 11	W/R	Unsign
\$1926	6438	2	OPERATOR MATHEMATHICS 12	W/R	Unsign
\$1928	6440	2	OPERATOR MATHEMATHICS 13	W/R	Unsign
\$192A	6442	2	OPERATOR MATHEMATHICS 14	W/R	Unsign
\$192C	6444	2	OPERATOR MATHEMATHICS 15	W/R	Unsign
\$192E	6446	2	OPERATOR MATHEMATHICS 16	W/R	Unsign
\$1930	6448	2	0 = OFF (default) 1÷16 = Cnt.Tot 1÷1 0 = OFF (default) 1÷16 = Cnt.Tot 1÷1 17÷32 = Cnt.Par. 1 33÷48 = Derivate 1 49÷64 = Math. 1÷1	÷16 W/R ÷16 6	Unsign
\$1932	6450	2	OPERAND 2 MATHEMATHICS 2	W/R	Unsign
\$1934	6452	2	OPERAND 2 MATHEMATHICS 3	W/R	Unsign
\$1936	6454	2	OPERAND 2 MATHEMATHICS 4	W/R	Unsign
\$1938	6456	2	OPERAND 2 MATHEMATHICS 5	W/R	Unsign
\$193A	6458	2	OPERAND 2 MATHEMATHICS 6	W/R	Unsign
\$193C	6460	2	OPERAND 2 MATHEMATHICS 7	W/R	Unsign
\$193E	6462	2	OPERAND 2 MATHEMATHICS 8	W/R	Unsign
\$1940	6464	2	OPERAND 2 MATHEMATHICS 9	W/R	Unsign
\$1942	6466	2	OPERAND 2 MATHEMATHICS 10	W/R	Unsign
\$1944	6468	2	OPERAND 2 MATHEMATHICS 11	W/R	Unsign
\$1946	6470	2	OPERAND 2 MATHEMATHICS 12	W/R	Unsign
\$1948	6472	2	OPERAND 2 MATHEMATHICS 12 OPERAND 2 MATHEMATHICS 13	W/R	Unsign
\$1946 \$194A	6472	2	OPERAND 2 MATHEMATHICS 13 OPERAND 2 MATHEMATHICS 14	W/R	Unsign
					0
\$194C	6476	2	OPERAND 2 MATHEMATHICS 15	W/R	Unsign
\$194E	6478	2	OPERAND 2 MATHEMATHICS 16	W/R	Unsign
0 4 4 5 5		-	SERIAL COM 1		
\$1A00 \$1A02	6656 6658	2	ID ADDRESS 1 (default)÷247 0 = 1200 1 = 2400 2 = 4800 3 = 9600 4 = 19200 5 = 38400 (def.)	W/R W/R	Unsign Unsign
	6660		Barbon (def.) 6 = 57600 7 = 115200 PARITY 0 = None (def.)		

				1 = Odd 2 = Even		
\$1A06	6662	2	STOP BIT	1 (default)÷2	W/R	Unsigned
			RESET			
\$2FF0	12272	2	RESET TOTAL COUNTERS	0 = NO 1÷16 = TOT.CNT. n. 255 = ALL TOT.CNT.	w	Unsigned
\$2FF2	12274	2	RESET PARTIAL COUNTERS	0 = NO 1÷16 = PAR.CNT. n. 255 = ALL PAR.CNT.	w	Unsigned
\$2FF4	12276	2	RESET TIMEBANDS	0 = NO 1÷16 = ALARM n. 255 = ALL ALARMS	w	Unsigned
\$2FF6	12278	2	RESET ALARMS	0 = NO 1÷16 = ALARM n. 255 = ALL ALARMS	w	Unsigned
\$2FF8	12280	2	RESET MEMORY	255 = RESET	W	Unsigned
\$2FFA	12282	2	RESET SETUP	255 = RESET	W	Unsigned
\$2FFC	12284	2	RESET SYSTEM	255 = RESET	W	Unsigned

MEMORY RECORD

Register HEX	Register DEC	Word	Description	Measure Unit	W/R	Туре
\$0700	1792	2	ALARM STATE	0 = OFF 1 = ON	R	Unsigned
\$0702	1794	2	ALARM n. or SYSTEM in ALARM	1÷16 = Alarm n. 255 = System	R	Unsigned
\$0704	1796	2	ALARM DATE - DAY	1÷31	R	Unsigned
\$0706	1798	2	ALARM DATE - MONTH	1÷12	R	Unsigned
\$0708	1800	2	ALARM DATE - YEAR	0÷99	R	Unsigned
\$070A	1802	2	ALARM TIME - HOUR	0÷23	R	Unsigned
\$070C	1804	2	ALARM TIME - MINUTE	0÷59	R	Unsigned
\$070E	1806	2	ALARM TIME - SECOND	0÷59	R	Unsigned
\$8000	32768	2	NEXT RECORD	255 = JUMP	W	Unsigned

Note: see "Reading of the records in memory" chapter.

PASSWORD

Register HEX	Register DEC	Word	Description	Measure Unit	W/R	Туре
\$1B10	6928	2	ENABLE MODIFY OF SETUP BY PASSWORD	Stored Password (0÷9999)	W	Unsigned
\$1B12	6930	2	PASSWORD	0÷9999	W	Unsigned

Note: If the password has been set (other than 0000), sending the command \$ 1B10 - ENABLE MODIFY OF SETUP BY PASSWORD, enables for 5 minutes writing serial of the setup parameters: after this time, the writing of the parameters of setup will be inhibited until the next command \$ 1B10 is sent.

The sending of write commands, when the password is set, does not change the value of the parameters and results in an exception error 15 (\$ 0F) by the device if previously was not sent the \$1B10 command.

Info Device & Firmware Version

Register HEX	Register DEC	Word	Description	W/R	Note:
\$9000	36864	18	SERIAL NUMBER	R	Expressed in ASCII Code (1 char per dword) - Only numbers - Word: 00-05: Identity Code (016) 06-09: Production Year (19÷99) 10-13: Production Week of the year (01÷52) 14-17: Progressive Number (01÷99)
\$9012	36882	28	CONFIGURATION CODE	R	Expressed in ASCII Code (1 char per dword) (for details see the following table)
\$902E	36910	8	HW REVISION	R	Expressed in ASCII Code
\$9036	36918	8	HW CUSTOMIZATION	R	Expressed in ASCII Code

Configuration code

Device	Digital IO	Serial Com.	Supply
EML16-	00: 16DI 24Vac/dc + 6DO 24Vdc 01: 16DI 48Vac/dc + 6DO 24Vdc 02: 16DI 115Vac/dc + 6DO 24Vdc 03: 16DI 230Vac/dc + 6DO 24Vdc 04: 14DI 24Vac/dc + 8DO 24Vdc 05: 14DI 48Vac/dc + 8DO 24Vdc 06: 14DI 115Vac/dc + 8DO 24Vdc 07: 14DI 230Vac/dc + 8DO 24Vdc	00: Not present 01: COM1 02: COM1+COM2	00: 115/230Vac 01: 20÷60Vac/dc 02: 90÷250Vac/dc

Boot Version

Register HE		Word	Description	W/R	Note:			
\$903E	36926	2	BOOT VERSION	R				

Firmware Version

Register HEX	Register DEC	Word	Description	W/R	Note:
\$9040	36928	2	FIRMWARE VERSION	R	

TROUBLESHOOTING

If response from Data Concentrator-EML16 doesn't happen:

- check connection from Data Concentrator-EML16 and RS232/RS485 converter;

- check if data outgoing from the RS232 serial port of the PC come in the RS232/485 converter
- try to increase the wait time for response (1000ms is good);
- check if the transmitted data stream is **EXACTLY** as in example, monitoring the data on the RS485 serial line with a terminal (i.e. Hyperterminal or other emulator);
- if the RS232/485 converter is not our model EMI-1, be sure the turnaround-time is set in 1ms ÷ 2ms range

